

SY205214ABC Ultra-Low Capacitance TVS Protection

General Description

SY205214ABC is a low-capacitance transient voltage suppressor (TVS) designed to provide electrostatic discharge (ESD) protection for high-speed data interfaces. With a typical capacitance of 0.6pF, SY205214ABC is designed to protect against overvoltage and over-current transient events. It complies with IEC 61000-4-2 (ESD) (±20kV air, ±20kV contact discharge), IEC 61000-4-5 (surge) (3A, 8/20µs).

The combined features of low capacitance, small size, and high ESD robustness make SY205214ABC ideal for high-speed data ports and high-frequency lines (e.g., HDMI and DVI) applications. The low clamping voltage of the SY205214ABC guarantees minimum stress on the protected IC.

Each SY205214ABC device can protect four high-speed data lines. The SY205214ABC is available in a compact SOT23-6 package.

Features

- Transient protection for High-Speed data lines
 - IEC 61000-4-2 (ESD) ±20kV (Air) ±20kV (Contact)
 - IEC 61000-4-5 (Surge) 3A (8/20µs)
- For operating voltage of 5V and below
- Small package (2.9mm × 2.8mm × 1.4mm)
- Protects four data lines
- Low capacitance: 0.6pF typical (I/O-GND)
- Low leakage current: 0.1µA @ V_{RWM} (Typical)
- Low clamping voltage
- Each I/O pin can withstand over 1000 ESD strikes for ±8kV contact discharge.

Applications

- Serial ATA
- PCI Express
- Desktops, Servers, and Notebooks
- MDDI Ports
- USB2.0 Power and Data Line Protection
- Display Ports
- High-Definition Multi-Media Interface (HDMI)
- Digital Visual Interfaces (DVI)

Mechanical Characteristics

- SOT23-6 package
- Flammability rating: UL 94V-0
- Marking: Part Number, Date
- Packaging: Tape and Reel

Circuit Diagram





SY205214ABC

Ordering Information

Part Number	Package Type	Top Mark	
SV205214ABC	SOT23-6	6112/0//0/	
31203214ABC	RoHS Compliant and Halogen Free	001000	

Pinout (Top View)



Marking Codes



Note 1: "6U" is device code, fixed.

Note 2: "YWW" is date code.

Absolute Maximum Rating							
Parameter	Symbol	Min	Мах	Unit			
Maximum Peak Pulse Current (8/20µs)	IPP		3	А			
Maximum Peak Pulse Power (8/20µs)	Ррк		.36	W			
ESD per IEC 61000-4-2 (Air)		20	20				
ESD per IEC 61000-4-2 (Contact)	VESD	-20	20	ĸv			
Operating Temperature	Торт	-40	+125	°C			
Storage Temperature	Tstg	-55	+150	°C			

Electrical Characteristics (T _A = 25°C)								
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit		
Nominal Reverse Working Voltage	Vrwm				5.0	V		
Reverse Leakage Current @ V _{RWM}	IR	V _{RWM} = 5V, T = 25°C Between I/O and GND		0.1	1.0	μA		
Reverse Breakdown Voltage @ I⊤	V _{BR}	I⊤ = 1mA Between I/O and GND	6.0	8.0	10.0	V		
Clamping Voltage @ IPP	Vc (1)	I _{PP} = 3A, t _p = 8/20µs Between I/O and GND		10	12	V		
Clamping Voltage @ IPP	Vc(1)	I _{PP} = 16A, t _p = 10/100ns Between I/O and GND		10.5	12.5	V		
Dynamic Resistance	R _{DYN} (1) (2)	t _p = 10/100ns Between I/O and GND		0.2		Ω		
Parasitic Capacitance	Cesd(1)	V _R = 0V, f = 1MHz Between I/O and GND		0.6	0.8	pF		
Parasitic Capacitance	C _{ESD} (1)	V _R = 0V, f = 1MHz Between I/O and I/O		0.25	0.4	pF		

Note 1: Guaranteed by design and not subject to production test.

Note 2: R_{DYN} calculated based on IPP=8A to IPP=16A, t_p = 10/100ns.





Figure 1. Uni-directional TVS



Typical Characteristics









ESD Clamping of I/O to GND (+8kV Contact per IEC 61000-4-2)



Insertion Loss S21 of I/O to GND



Clamping Voltage vs. Peak Pulse Current (8/20µs)



ESD Clamping of I/O to GND (-8kV Contact per IEC 61000-4-2)





Application Information

Pin Connections

SY205214ABC is designed to provide ESD protection for four data lines simultaneously. The pin connections are shown in Figure 2.

The four parallel data lines can be connected from the protected IC to the I/O port connector and directly to the four SY205214ABC I/O pins. Pin2 of SY205214ABC is the negative reference pin, which should connect to the ground. The connection wires should be as short as possible to minimize the parasitic inductance.



Figure 1. SY205214ABC Pin Connections in PCB

PCB Layout Guidelines

For optimum ESD protection and circuit performance, the following PCB layout guidelines are recommended:

- Place SY205214ABC as close to the connector port as possible.
- The distance between the SY205214ABC ground pin and the GND reference path should be as short as possible.
- Use a large via to connect the SY205214ABC VCC and GND pins to the PCB VCC and GND.
- Avoid running critical signals near board edges.



Figure 3. SY205214ABC Layout Guideline



Universal Serial Bus ESD Protection



Application Information



Layout Top View for Video (VGA) Interface with SY205214ABC









SOT23-6 Package Outline







F

SIDE VIEW



Symbol	Dimensions In Millimeters			
	Minimum	Maximum		
А	1.00	1.30		
A1	0.01	0.10		
D	2.80	3.10		
E	2.70	3.00		
E1	1.50	1.70		
b	0.30	0.50		
е	0.95 TYP			
e1	1.90 TYP			
L	0.30	0.60		
L1	0.25 TYP			
L2	0.10	0.15		

Symbol	Dimensions			
	Millimete	Inches		
А	0.60	0.024		
В	1.10	0.043		
С	0.95	0.037		
D	1.40	0.055		
E	2.50	0.098		

Note: All dimensions are in millimeters and exclude mold flash and metal burr.

Package Dimensions



Tape and Reel Specification

SOT23-6 Taping Orientation



Carrier Tape & Reel Specification for Packages



Symbol	Reel Size	М	Ν	W	W1	Н	S	K	R
Dimensions (mm)	Φ178	178.0±1.0	60.0±1.0	11.5±0.5	9.0±0.5	13.0±0.5	2.0±0.1	11.0±0.2	1.0±0.05

Package Types	Tape Width (mm)	Pocket Pitch(mm)	Reel Size (Inch)	Qty per Reel (pcs)
SOT23-6	8	4	7"	3000



Revision History

The revision history provided is for informational purpose only and is believed to be accurate, however, not warranted. Please make sure that you have the latest revision.

Revision Number	Revision Date	Description	Pages changed
0.9	10/14/2016	Initial Release	
1.0	10/14/2017	Production Release	



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