SYCT400

400A High Accuracy Galvanically Isolated Hall Current Sensor IC

General Description

The SYCT400 is a high accuracy galvanically isolated Hall-effect current sensor ICs for 400A. Current sensing supports both unidirectional and bidirectional, DC and AC currents. Applied current generates a magnetic field, then converted into a proportional analog voltage. The bandwidth is up to 170kHz and the response time is 3µs.

The SYCT400 provides a ratiometric or fixed zero current output for differential measurements. A precise, proportional output voltage is achieved by the high accuracy, good linearity, low temperature drift Hall IC, which is programmed for accuracy at the factory.

The package offers four options, PFF/PSF/PSS/SMT, for easy implementation, which the PFF is the standard package.

The SYCT400 is suitable for insulation, small size with low heat generation applications, including automotive, industrial, and communications systems.

Features

- 5V single supply
- Wide Hall current sensing range:
  - Bidirectional: ±400A
  - Unidirectional: 400A
- 170kHz typical bandwidth, response time 0.7µs
- UL60950-1(ed.2) certified
  - Dielectric Strength Test Voltage = 4800V_{RMS}
  - Working Voltage for Basic Isolation = 700V_{RMS}
  - Working Voltage for Reinforced Isolation = 450V_{RMS}
- Ultralow primary conductor resistance for power loss
- Factory-trimmed for high accuracy
- Package: PFF/PSF/PSS/SMT

Applications

- EV/HEV charger and DC-DC power supply
- Photovoltaic inverter power supply and UPS
- Motor control and frequency converter
- Communication and server power supply

Typical Application

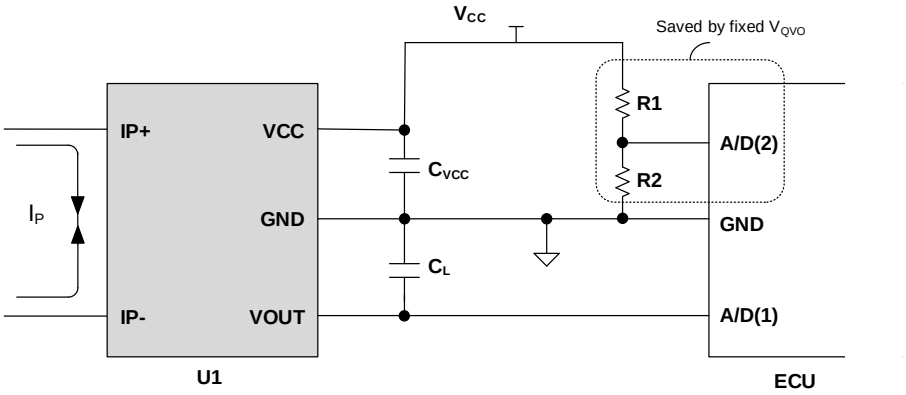


Figure 1. Typical Application Circuit

Package Type

PFF
(Standard)

PSF

PSS

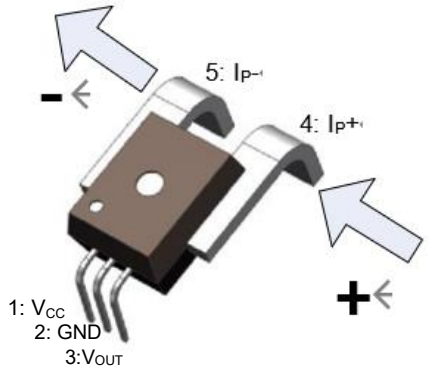
SMT

Ordering Information

Please indicate the package type in the suffix when ordering, such as SYCT400BR-PSF.
If the packaging type is not specified, the default is to the standard (PFF) package.

Part Number	Architecture	I_P(A)	Sensitivity(mV/A)	V_{QVO}(V)	T_A(℃)
SYCT400BR	Bidirectional	±400	5	Vcc/2	-40 to 85
SYCT400BF	Bidirectional	±400	5	2.5	-40 to 85
SYCT400UR	Unidirectional	400	10	Vcc/10	-40 to 85
SYCT400UF	Unidirectional	400	10	0.5	-40 to 85

Pinout (Top View)



Pin Number	Pin Name	Pin Description
1	VCC	Power supply pin.
2	GND	Ground pin.
3	VOUT	Analog output pin.
4	IP+	Positive terminals for sensing current.
5	IP-	Negative terminals for sensing current.

Absolute Maximum Ratings (Note 1)

Supply Input Voltage ---------- -0.3 to 6.5V

Supply Input Current ---------- 18mA

Output Voltage ---------- 0.15 to V_{CC}-0.15V

Output Current (Note 2) ---------- ±40 mA

Ambient Temperature Range ---------- -40 to 85°C

Maximum Junction Temperature ---------- 165°C

Storage Temperature Range ---------- -55 to 150°C

Recommended Operating Conditions (Note 3)

Supply Input Voltage ---------- 4.5 to 5.5V

Isolation Characteristics

Characteristic	Symbol	Notes	Rating	Unit
Dielectric Strength Test Voltage (Note 4)	V_{ISO}	Agency type-tested for 60 seconds per UL standard 60950-1 (edition 2)	4800	V_{RMS}
Working Voltage for Basic Isolation	V_{WVBI}	For basic (single) isolation per UL standard 60950-1 (edition 2)	990	V_{PK}
			700	V_{RMS}
Working Voltage for Reinforced Isolation	V_{WVRI}	For reinforced (double) isolation per UL standard 60950-1 (edition 2)	636	V_{PK}
			450	V_{RMS}

Electrical Characteristics

(V_{CC} = 5V, C_{VCC} = 0.1μF, T_A = -40 to 125°C, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ.	Max	Unit
Supply Voltage	V_{CC}		4.5	5	5.5	V
Supply Current	I_{CC}	$R_L \geq 10k\Omega$		13	18	mA
Power on Delay Time	T_{POD}	T_A=25°C		80		μs
Zero Current Output Voltage (Note 5)	V_{QVO}	SYCT400BR, T_A = 25°C		V_{CC}/2		V
		SYCT400BF, T_A = 25°C		2.50		V
		SYCT400UR, T_A = 25°C		V_{CC}/10		V
		SYCT400UF, T_A = 25°C		0.50		V
Output Voltage Range @I_P	V_{OUT}-V_{QVO}	SYCT400BR, T_A = 25°C		±2		V
		SYCT400BF, T_A = 25°C		±2		V
		SYCT400UR, T_A = 25°C		4		V
		SYCT400UF, T_A = 25°C		4		V
Zero Current Output Ratiometry Error	E_{RAT}		-0.3		0.3	%
Output Load Resistance	R_L	V_{OUT} to V_{CC} or GND	10			kΩ
Output Load Capacitance	C_L	V_{OUT} to GND	6		100	nF
Response Time	t_{RES}	T_A=25°C, C_L=1nF		3		μs
Bandwidth	BW	Small signal –3dB, C_L=1nF, T_A=25°C	120	170		kHz
DC Output Impedance	R_{OUT}	T_A = 25°C		8		Ω

Note 1: Stresses beyond the "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note 2: Maximum survivable sink or source current on the VOUT pin.

Note 3: The device is not guaranteed to function outside its operating conditions.

Note 4: 60-second test is only for UL test; Tested in production against UL60950-1 2nd Edition.

SYCT400BR and SYCT400BF Performance Characteristics

(V_{CC} = 5V, C_{VCC} = 0.1μF, T_A = -40 to 85°C, unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ.	Max	Unit
Nominal Performance						
Current Sensing Range	I_P		-400		400	A
Sensitivity	Sens			5		mV/A
Accuracy Performance						
Sensitivity Error	E_{SENS}	T_A=25°C	-1		1	%
Voltage Offset Error	V_{OE}	I_P=0A, T_A = 25°C	-4	±3	4	mV
		I_P=0A	-20	±6	20	mV
Magnetic Offset Error	I_{OM}	I_P=0A, T_A = 25°C, after excursion of 400A		800	1000	mA
Offset Current	I_{OE}	T_A = 25°C			1.5	A
Linearity Error	E_{LIN}	Full scale of I_P	-1	0.5	1	%
Total Output Error	$E_{TOT, H}$	Full scale of I_P, T_A = 25 to 85°C	-2		2	%
	$E_{TOT, L}$	Full scale of I_P, T_A = –40 to 25°C	-2		2	%

SYCT400UR and SYCT400UF Performance Characteristics

(V_{CC} = 5V, C_{VCC} = 0.1μF, T_A = -40 to 85°C, unless otherwise specified)

Parameter	Symbol	Condition	Min	Typ.	Max	Unit
Nominal Performance						
Current Sensing Range	I_P		0		400	A
Sensitivity	Sens			10		mV/A
Accuracy Performance						
Sensitivity Error	E_{SENS}	T_A=25°C	-1		1	%
Voltage Offset Error	V_{OE}	I_P=0A, T_A = 25°C	-4	±3	4	mV
		I_P=0A	-20	±6	20	mV
Magnetic Offset Error	I_{OM}	I_P=0A, T_A = 25°C, after excursion of 400A		400	500	mA
Offset Current	I_{OE}	T_A = 25°C			1	A
Linearity Error	E_{LIN}	Full scale of I_P	-1	0.5	1	%
Total Output Error	$E_{TOT, H}$	Full scale of I_P, T_A = 25 to 85°C	-2		2	%
	$E_{TOT, L}$	Full scale of I_P, T_A = –40 to 25°C	-2		2	%

Operation

The SYCT400 is a family of high accuracy galvanically isolated current sensor ICs. There are various output modes for measuring bidirectional or unidirectional current from 50A to 250A, which output the analog voltage proportional to the AC or DC current. The bandwidth is up to 170kHz.

A precise, proportional output voltage is achieved by the high accuracy Hall IC, which is trimmed for accuracy at the factory.

The package offers four options, PFF/PSF/PSS/SMT, for easy implementation, which the PFF is the standard package.

Application Information

Sensitivity (Sens)

Sensitivity is the slope of the approximate straight line between the output voltage (V_{OUT}) and the input primary current (I_P) within the sensing range. V_{QVO} is the quiescent voltage output.

$$V_{OUT} = V_{QVO} + Sens \times I_P$$

Sensitivity Error (E_{SENS})

The sensitivity error (E_{SENS}) is the percent difference between the measured sensitivity ($Sens_M$) and the ideal value ($Sens_{TYP}$).

$$E_{SENS} = \frac{Sens_M - Sens_{TYP}}{Sens_{TYP}} \times 100\ (\%)$$

Response Time (t_{RES})

Response time (t_{RES}) is defined as the time delay from the 90% of input primary current (I_P) to the 90% of the output voltage (V_{OUT}).

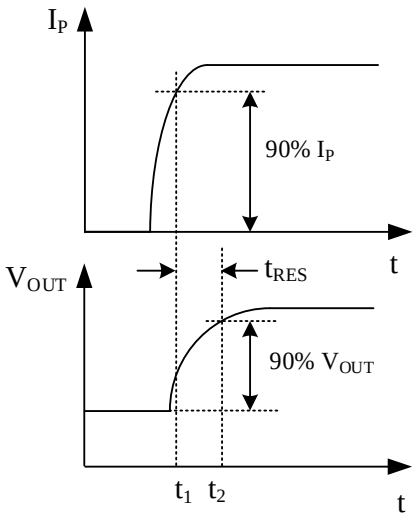


Figure 3. Response Time

Quiescent Voltage Output (V_{QVO})

The V_{QVO} is defined as the output voltage (V_{OUT}) when the primary current (I_P) is zero. It is related to V_{CC}, current direction, and internal logic. The relationship is shown as below:

Part Number	V_{QVO}
SYCT400BR	$V_{CC}/2$
SYCT400BF	2.50V
SYCT400UR	$V_{CC}/10$
SYCT400UF	0.50V

Voltage Offset Error (V_{OE})

The voltage offset error (V_{OE}) is defined as the deviation of the zero current output voltage from its ideal quiescent value (V_{QVO}), due to nonmagnetic causes.

Zero Current Output Ratiometric Error (E_{RAT})

When the supply voltage V_{CC} changes from the typical value ($V_{CC,TYP}$) to $V_{CC,MIN} < V_{CC,M} < V_{CC,MAX}$ there will be a certain deviation between the measured zero current output ($V_{QVO,M}$) and its ideal value ($V_{QVO,TYP}$). The relationship is shown as below:

$$E_{RAT} = 1 - \frac{V_{QVO,M}}{V_{CC,M}} \times \frac{V_{CC,TYP}}{V_{QVO,TYP}}$$

Magnetic Offset Error (I_{OM})

The magnetic offset is due to the remnant field of the core material. The magnetic offset error is highest when the primary current approaches zero from the maximum value.

Linearity Error (E_{LIN})

The linearity error (E_{LIN}) is the maximum positive or negative error between the measured output voltage and the ideal value within the sensing range.

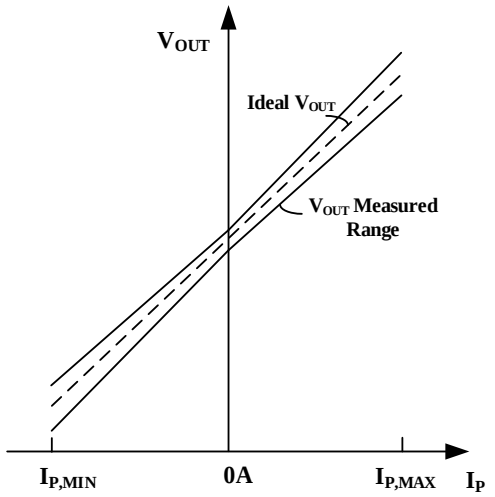


Figure 4. Linearity Error

Total Output Error (E_{TOT})

Total output error (E_{TOT}) is defined as the difference between the ideal output voltage (V_{OUT}) and the actual output voltage ($V_{OUT,IP}$), divided by the ideal sensitivity (Sens), relative to the primary current (I_P) flowing through the primary conduction path:

$$E_{TOT} = \frac{V_{OUT,IP} - V_{OUT}}{Sens \times I_P}$$

At relatively high current, E_{TOT} will be mostly due to sensitivity errors; but at relatively low current, E_{TOT} will be mostly due to the voltage offset error (V_{OE}). As the primary current (I_P) approaches zero, the E_{TOT} approaches infinity due to the voltage offset error (V_{OE}).

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Layout Design

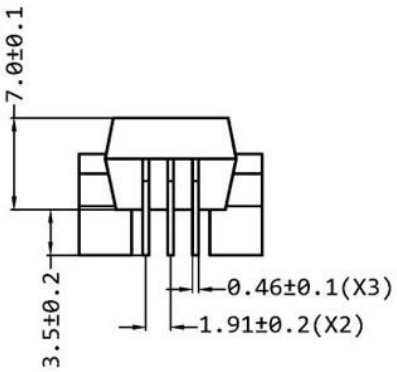
The layout design of the SYCT400 is relatively simple. For the best performance, the attention should be paid to heat dissipation and traces.

1. It is best practice for the current to approach the IC parallel to the current-carrying pins, and for the current-carrying trace to not creep towards the center of the package.

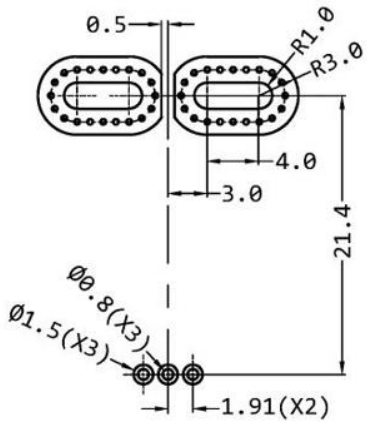
2. It is best to place C_{VCC} and C_L close to the IC to achieve better filtering performance

Package outline & PCB Layout Design

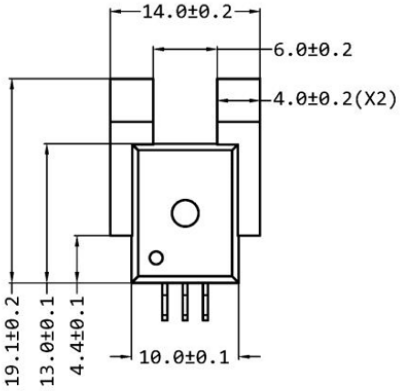
Standard (PFF) Package



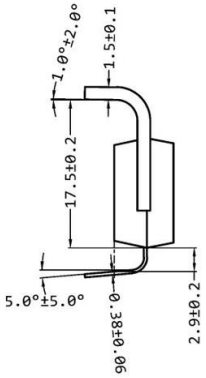
Side View



PCB layout design



Top View

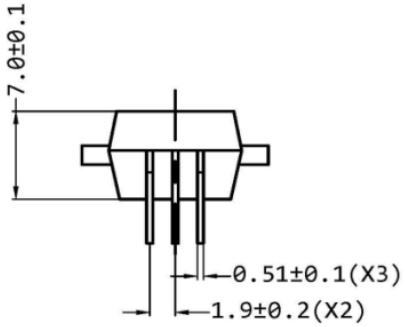


Side View

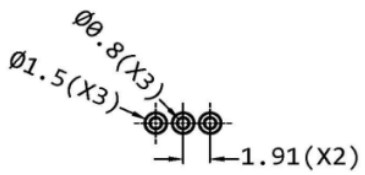
Terminals	Definitions
1	Vcc
2	GND
3	Vout
4	IP+
5	IP-

Notes: **All dimensions are in millimeters.**
All dimensions don't include mold flash & metal burr.

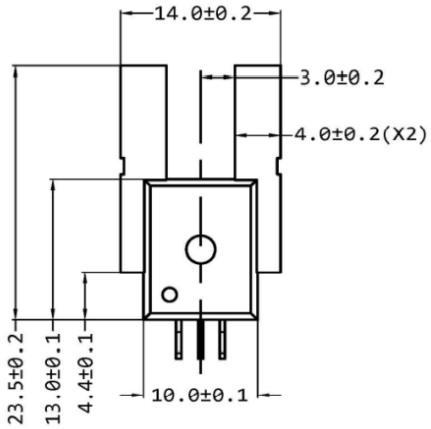
PSF Package



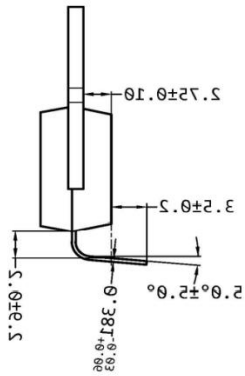
Side View



PCB layout design



Top View



Side View

Terminals	Definitions
1	Vcc
2	GND
3	Vout
4	IP+
5	IP-

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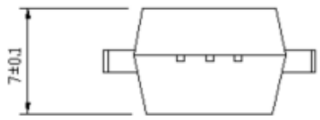
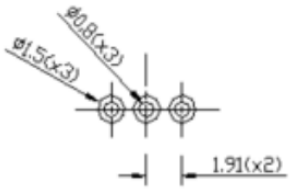
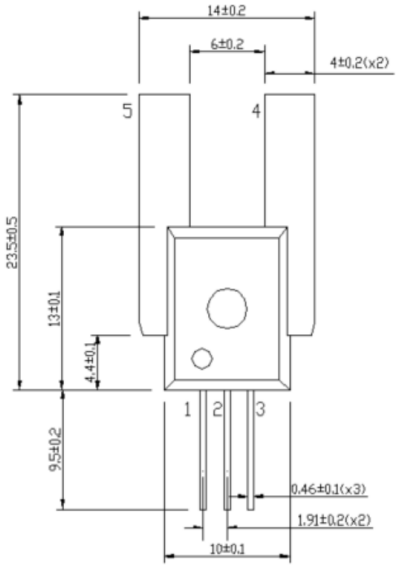
PSS Package



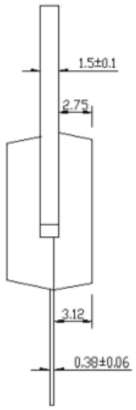
Side View



PCB layout design



Top View

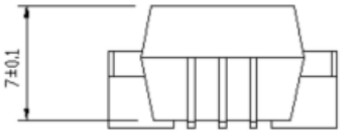


Side View

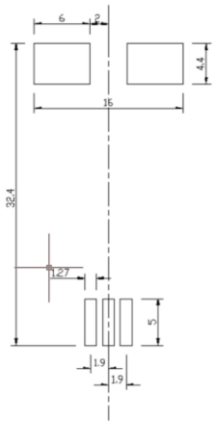
Terminals	Definitions
1	VCC
2	GND
3	VOUT
4	IP+
5	IP-

Notes: **All dimensions are in millimeters.**
All dimensions don't include mold flash & metal burr.

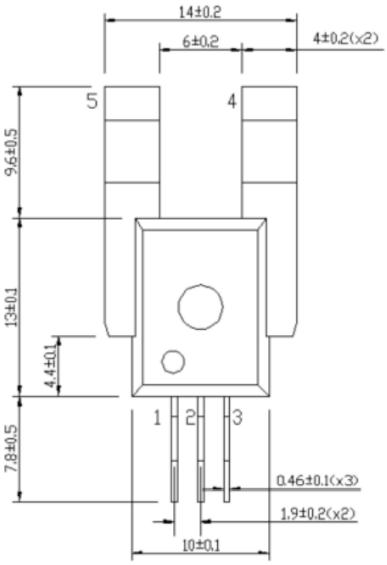
SMT Package



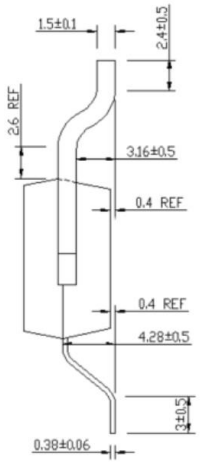
Side View



PCB layout design



Top View



Side View

Terminals	Definitions
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Notes: **All dimensions are in millimeters.**
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