



# SYS42X12TPD

## Single Line TVS Diode for ESD Protection

### General Description

The SYS42X12TPD is a single line Transient Voltage Suppressor (TVS) designed to provide Electrical Over Stress (EOS) protection for cell phones, notebook computers, PDA's. The SYS42X12TPD is designed to protect sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other over-current transient events. It complies with IEC 61000-4-2 (ESD)( $\pm 30\text{kV}$  air,  $\pm 30\text{kV}$  contact discharge), IEC 61000-4-5 (Surge) 135A (8/20 $\mu\text{s}$ ), etc.

The SYS42X12TPD is available in a DFN2.0x2.0-3 package with an operating voltage of 12V. The SYS42X12TPD protects a single, unidirectional line.

### Features

- Operating Voltage: 12V and Below
- Transient Protection for A Single Line
  - IEC 61000-4-2 (ESD)  $\pm 30\text{kV}$  (Air)  $\pm 30\text{kV}$  (Contact)
  - IEC 61000-4-5 (Surge) 135A (8/20 $\mu\text{s}$ )
- Low Capacitance: 1000pF (Typical)
- Low Leakage Current: 0.01 $\mu\text{A}$  @  $V_{\text{RWM}}$  (Max)
- Low Clamping Voltage
- MSL Rating: MSL1

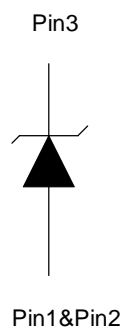
### Applications

- Cell Phone Handsets and Accessories
- Power Supply Protection
- Portable Instrumentation
- Pagers Peripherals
- Digital Cameras

### Mechanical Characteristics

- DFN 2.0x2.0-3 Package
- Marking: Device Code, Date Code
- Packaging: Tape and Reel

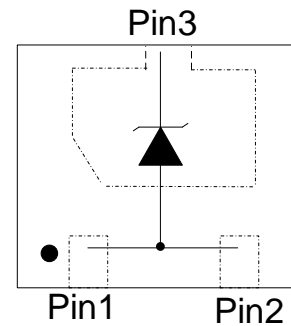
### Circuit Diagram



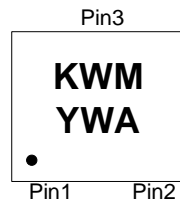
## Ordering Information

Part Number	Package Type	Top Mark
SYS42X12TPD	DFN2.0x2.0-3	KWMYWA

## Pinout (Top View)



## Marking Codes



Notes: "KWM" is the device code, fixed.

"YWA" is the date code.

Absolute Maximum Ratings(Note 1)				
Parameter	Symbol	Min	Max	Unit
Peak Pulse Current (8/20 $\mu$ s)	$I_{PP}$		135	A
Peak Pulse Power (8/20 $\mu$ s)	$P_{PK}$		2600	W
ESD per IEC 61000-4-2 (Air)	$V_{ESD}$	-30	30	kV
ESD per IEC 61000-4-2 (Contact)		-30	30	
Junction Temperature	$T_J$	-45	+125	$^{\circ}$ C
Storage Temperature	$T_{STG}$	-55	+150	$^{\circ}$ C

Electrical Characteristics (From Pin3 to Pin1 and Pin2 $T_A = 25^{\circ}$ C, Note 4)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Nominal Reverse Working Voltage	$V_{RWM}$				12	V
Reverse Leakage Current at $V_{RWM}$	$I_R$	$V_{RWM} = 12V, T_A = 25^{\circ}C$		0.01	1	$\mu$ A
Reverse Breakdown Voltage at $I_T$	$V_{BR}$	$I_T = 1mA$	13		18	V
Forward Voltage at $I_F$	$V_F$	$I_F = 1mA$ From Pin1 or Pin2 to Pin3	0.55		0.85	V
Dynamic Resistance (Note 2, 5)	$R_{DYN}$	$t_p = 10/100ns$		0.01		$\Omega$
Clamping Voltage at $I_{PP}$ (Note 5)	$V_C$	$I_{PP} = 135A, t_p = 8/20\mu s$		17.5		V
Clamping Voltage at $I_{PP}$ (Note 5)	$V_C$	$I_{PP} = 16A, t_p = 10/100ns$		14		V
Parasitic Capacitance (Note 5)	$C_{ESD}$	$V_R = 0V, f = 1MHz$		1000	1200	pF

**Note 1:** Stresses beyond the “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**Note 2:**  $R_{DYN}$  calculated based on  $I_{PP}=8A$  to  $I_{PP}=16A$ ,  $t_p = 10/100ns$ .

**Note 3:** The device is not guaranteed to function outside its operating conditions.

**Note 4:** Unless otherwise stated, limits are 100% production tested under pulsed load conditions such that  $T_A \cong T_J = 25^\circ C$ . Limits over the operating temperature range (see recommended operating conditions) and relevant voltage range(s) are guaranteed by design, test, or statistical correlation.

**Note 5:** Guaranteed by design or statistical correlation and not production tested.

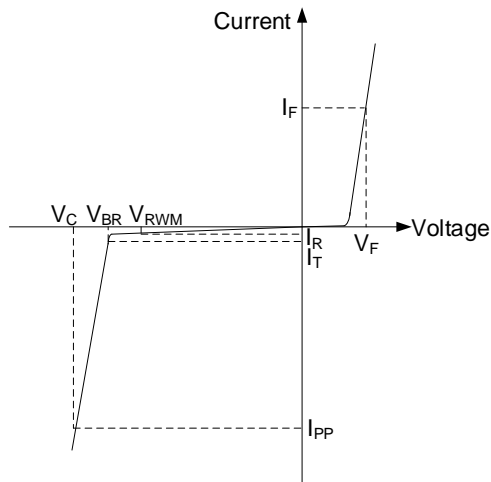
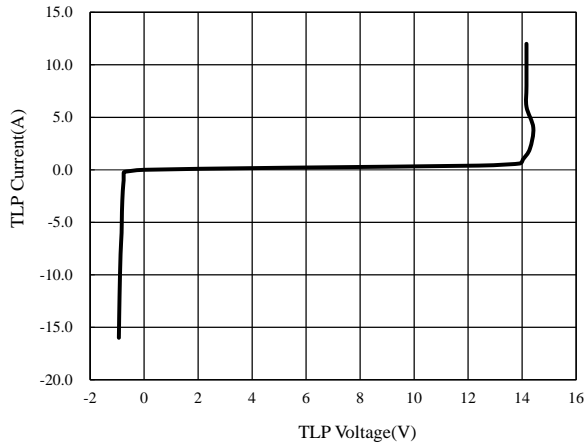


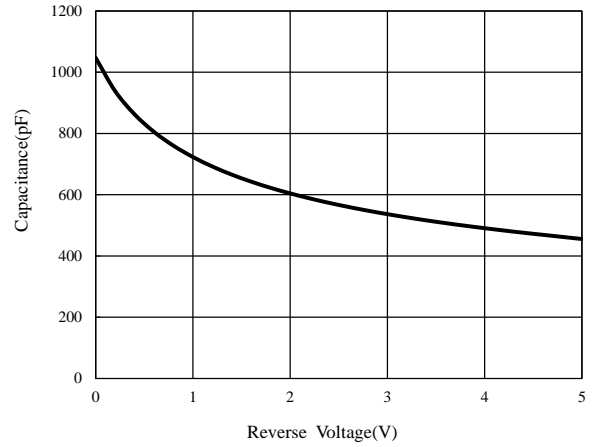
Figure 1. Uni-Directional TVS

## Typical Performance Characteristics (Pin3 to Pin1 and Pin2)

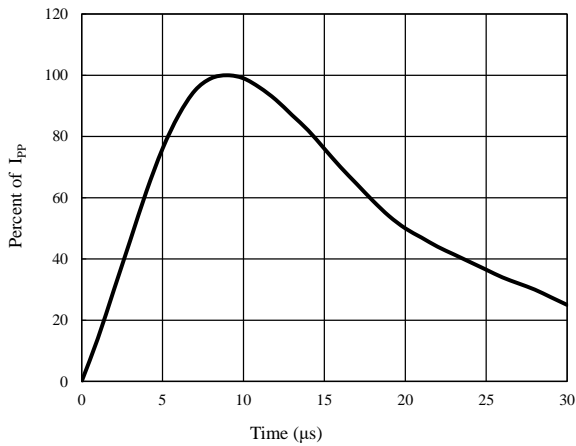
### TLP Testing



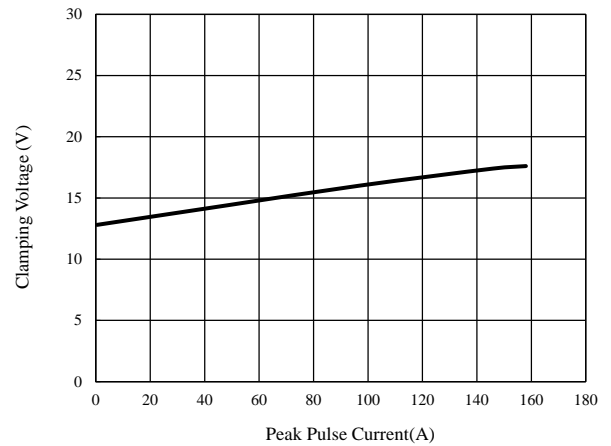
### Capacitance vs. Voltage



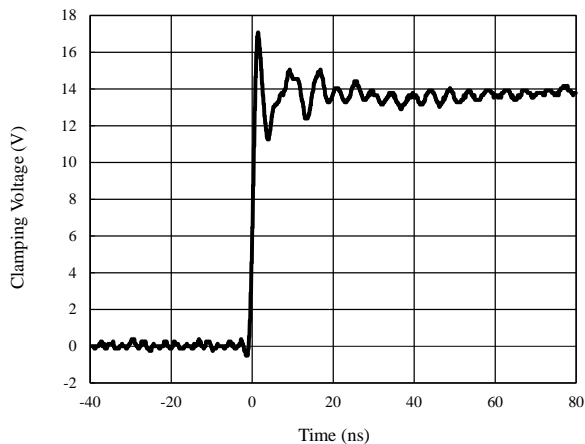
### Pulse Waveform



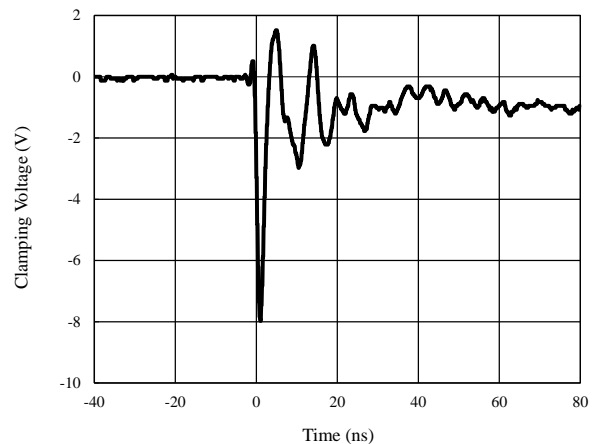
### Clamping Voltage vs. Peak Pulse Current



### ESD Clamping (+8kV Contact per IEC 61000-4-2)



### ESD Clamping (-8kV Contact per IEC 61000-4-2)



## Application Information

### PCB Pin Connections

The SYS42X12TPD can be used to protect one unidirectional line. It offers system designers flexibility to protect a power line. The power line can be connected directly to pin3. Pins 1 2 of the SYS42X12TPD are the negative reference pin, which should connect to the GND. The connection traces should be as short as possible to minimize the parasitic inductance.

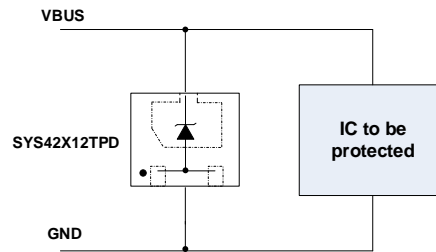


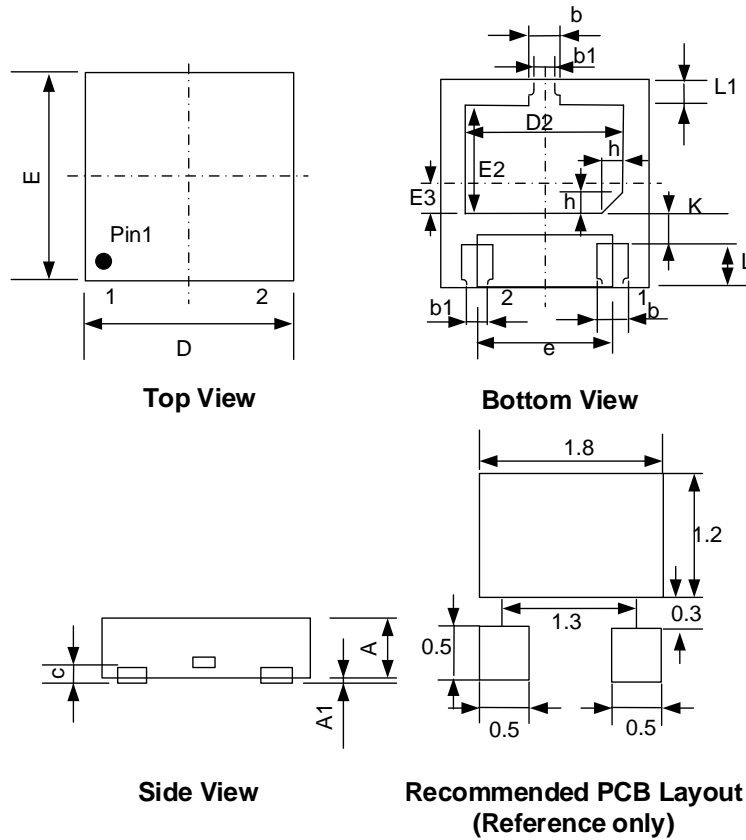
Figure 2. SYS42X12TPD Pin Connection in PCB

### PCB Layout Guidelines

For optimum ESD protection and circuit performance, the following PCB layout guidelines are recommended:

- The distance between pins 2/3 and the PCB GND rail path should be as short as possible to reduce the ESD transient return path to the GND.
- Use large vias to connect pins 1 and 2/3 to the PCB power and GND lines respectively.
- Place the SYS42X12TPD as close as possible to the connector port to reduce parasitic inductance and prevent ESD coupling into adjacent traces.

## DFN2.0x2.0-3 Package Outline



### Package Dimensions

Symbol	Dimensions in Millimeters		
	MIN	NOM	MAX
A	0.50	0.55	0.60
A1	0.00	0.02	0.05
b	0.25	0.30	0.35
b1	0.20 REF		
c	0.152 REF		
D	1.90	2.00	2.10
D2	1.40	1.50	1.60
e	1.30BSC		
E	1.90	2.00	2.10
E2	0.95	1.05	1.15
E3	0.20	0.30	0.40
L	0.35	0.40	0.45
L1	0.20	0.25	0.30
h	0.20 REF		
K	0.20	0.30	0.40

Note: All dimensions are in millimeters and exclude mold flash and metal burr.





## Revision History

The revision history provided is for informational purposes only and is believed to be accurate; however, not warranted. Please make sure that you have the latest revision.

Revision Number	Revision Date	Description	Pages changed
1.0	May. 20, 2025	Initial Release	



## IMPORTANT NOTICE

1. **Right to make changes.** Silergy and its subsidiaries (hereafter Silergy) reserve the right to change any information published in this document, including but not limited to circuitry, specification and/or product design, manufacturing or descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All semiconductor products are sold subject to Silergy's standard terms and conditions of sale.

2. **Applications.** Application examples that are described herein for any of these products are for illustrative purposes only. Silergy makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification. Buyers are responsible for the design and operation of their applications and products using Silergy products. Silergy or its subsidiaries assume no liability for any application assistance or designs of customer products. It is customer's sole responsibility to determine whether the Silergy product is suitable and fit for the customer's applications and products planned. To minimize the risks associated with customer's products and applications, customer should provide adequate design and operating safeguards. Customer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences, lessen the likelihood of failures that might cause harm and take appropriate remedial actions. Silergy assumes no liability related to any default, damage, costs or problem in the customer's applications or products, or the application or use by customer's third-party buyers. Customer will fully indemnify Silergy, its subsidiaries, and their representatives against any damages arising out of the use of any Silergy components in safety-critical applications. It is also buyers' sole responsibility to warrant and guarantee that any intellectual property rights of a third party are not infringed upon when integrating Silergy products into any application. Silergy assumes no responsibility for any said applications or for any use of any circuitry other than circuitry entirely embodied in a Silergy product.

3. **Limited warranty and liability.** Information furnished by Silergy in this document is believed to be accurate and reliable. However, Silergy makes no representation or warranty, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. In no event shall Silergy be liable for any indirect, incidental, punitive, special or consequential damages, including but not limited to lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges, whether or not such damages are based on tort or negligence, warranty, breach of contract or any other legal theory. Notwithstanding any damages that customer might incur for any reason whatsoever, Silergy' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Standard Terms and Conditions of Sale of Silergy.

4. **Suitability for use.** Customer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of Silergy components in its applications, notwithstanding any applications-related information or support that may be provided by Silergy. Silergy products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of a Silergy product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Silergy assumes no liability for inclusion and/or use of Silergy products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

5. **Terms and conditions of commercial sale.** Silergy products are sold subject to the standard terms and conditions of commercial sale, as published at <http://www.silergy.com>, unless otherwise agreed in a valid written individual agreement specifically agreed to in writing by an authorized officer of Silergy. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Silergy hereby expressly objects to and denies the application of any customer's general terms and conditions with regard to the purchase of Silergy products by the customer.

6. **No offer to sell or license.** Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights. Silergy makes no representation or warranty that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right. Information published by Silergy regarding third-party products or services does not constitute a license to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from Silergy under the patents or other intellectual property of Silergy.

For more information, please visit: [www.silergy.com](http://www.silergy.com)

© 2025 Silergy Corp.

All Rights Reserved.