

General Description

The SY22601B is an adaptive linear current regulator to eliminate low frequency current ripple targeting at LED lighting applications.

It is applied as a current filter to the output of a LED driver, especially single stage LED driver. It adopts adaptive control scheme and no additional electrical design is needed.

Reliable open/short LED protection and over thermal protection are all provided.

Ordering Information

SY22601 □ (□ □) □
 └─┬─┬─┘
 Temperature Code
 Package Code
 Optional Spec Code

Ordering Number	Package type	Note
SY22601BFAC	SO8	----

Features

- Current Filter for Single Stage LED Driver to Eliminate Current Ripple
- Proprietary Scheme for Low Power Loss $\leq 2.5\%$
- Adaptive for Wide Output Speculation:
Output Voltage Range from 20V to 100V
Output Current $\leq 250\text{mA}$
- Open LED Protection and Short LED Protection
- Reliable Short LED and Open LED Protection
- RoHS Compliant and Halogen Free
- Compact Package: SO8

Applications

- LED Lighting

Typical Applications

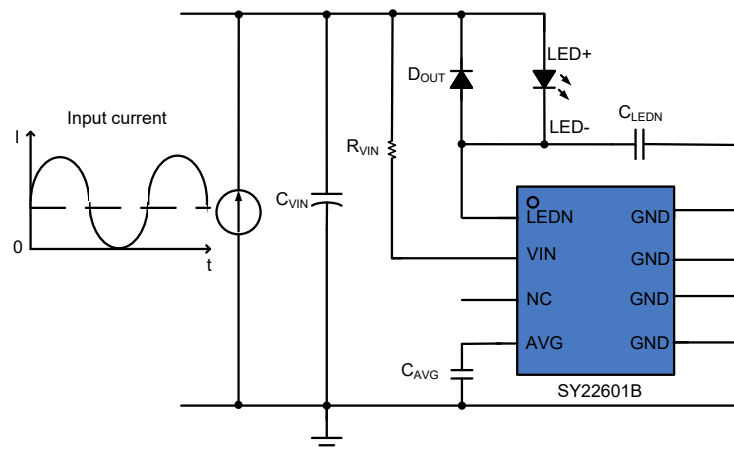
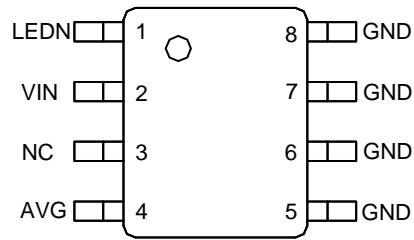


Figure 1. Schematic Diagram

Pinout (top view)

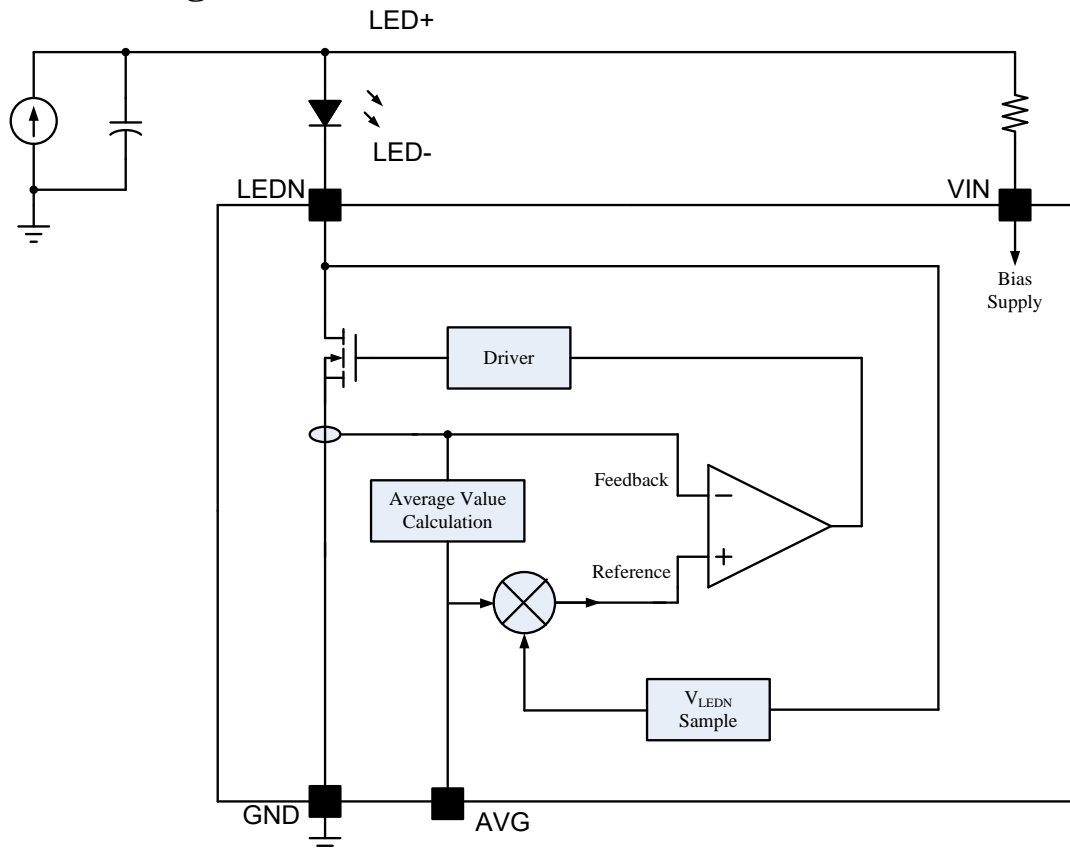


(SO8)

Top Mark: AZBxyz (device code: **AZB**, *x=year code*, *y=week code*, *z=lot number code*)

Pin Name	Pin Number	Pin Description
LEDN	1	Cathode of LED string.
VIN	2	Power Supply. Cascade a resistor (20KΩ) to this pin and anode of the LED string.
NC	3	NC.
AVG	4	Average current filter pin. Bypass a capacitor (100nF) to this pin and GND.
GND	5/6/7/8	Ground pin

Block Diagram





Absolute Maximum Ratings (Note 1)

VIN	-----	-0.3V~100V
LEDN	-----	-0.3~100V
Power Dissipation, @ T _A = 25°C SO8	-----	-0.6W
Package Thermal Resistance (Note 2)		
SO8, θ_{JA}	-----	88°C/W
SO8, θ_{JC}	-----	45°C/W
Junction Temperature Range	-----	-40°C to 150°C
Lead Temperature (Soldering, 10 sec.)	-----	260°C
Storage Temperature Range	-----	-65°C to 150°C

Recommended Operating Conditions

VIN, LEDN	-----	20V~100V
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Electrical Characteristics

($V_{IN} = 12V$, $T_A = 25^\circ C$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Power Supply Section						
VIN turn-on threshold	V_{VIN_ON}		9	10	11	V
VIN turn-off threshold	V_{VIN_OFF}		7	7.8	9	V
VIN operating current	I_{VIN}		55	77	90	μA
LEDN Section						
High Voltage Protection	V_{LEDN_HV}		9	10	11	V
Over Voltage Protection	V_{LEDN_OV}		13.5	14.5	15.5	V
Thermal Section						
Thermal Shutdown Temperature	T_{SD1}	$V_{LEDN} < V_{LEDN_OV}$		150		C
Thermal Shutdown Temperature	T_{SD2}	$V_{LEDN} > V_{LEDN_OV}$		100		C
Thermal Hysteresis Temperature	T_{HYS}			20		C

Note 1: Stresses beyond the “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note 2: Θ_{JA} is measured in the natural convection at $T_A = 25^\circ C$ on a low effective single layer thermal conductivity test board of JEDEC 51-3 thermal measurement standard. Test condition: Device mounted on 2” x 2” FR-4 substrate PCB, 2oz copper, with minimum recommended pad on top layer and thermal vias to bottom layer ground plane.

Operation

The SY22601B is an adaptive linear current regulator to eliminate low frequency current ripple targeting at LED lighting applications.

It is applied as a current filter to the output of a LED driver, especially single stage LED driver. It adopts adaptive control scheme and no additional electrical design is needed.

It is adaptive for wide output speculation, the output voltage is ranging from 20V to 100V; the maximum output current is 250mA. It adopts proprietary scheme for low power loss and the efficiency loss is no more than 2.5%. It also can be operated in parallel to support higher LED current.

SY22601B provides reliable protections such as Short LED Protection (SLP), Open LED Protection (OLP), and Over Temperature Protection (OTP).

SY22601B is available with SO8.

Applications Information

Start up

When V_{VIN} rises up over V_{VIN_ON} , internal blocks start to work. At first, it has certain blanking time without current filter function to build up stable average current reference internally. Then the LED current ripple is suppressed gradually.

Shut down

When V_{VIN} drops down below V_{VIN_OFF} , LEDN Pin is changed to high impedance to GND Pin.

Steady state

LED current is sampled and processed inside the IC. The average value of LED current is taken as the reference to regulate the instantaneous current. At the same time, V_{LEDN} is sensed and kept low to avoid high power loss on the IC.

If the input/output has certain dynamic turbulence, V_{LEDN} might be higher than V_{LEDN_HV} , then LED current ripple suppression will be loosen to make V_{LEDN} drop down quickly until V_{LEDN} is lower than V_{LEDN_HV} . Hence, the normal value of V_{LEDN} must be lower than V_{LEDN_HV} .

Open LED & Recover

When LED is open, LED current and V_{LEDN} both drop to zero naturally, the IC will quit the current filter function.

When LED is recovered, LED current is sensed and IC acts just like start up again.

Short LED & Recover

When LED is short, V_{LEDN} is pulled up high. If V_{LEDN} is higher than V_{LEDN_OV} , LED current will be limited at certain value and thermal shut down threshold is changed down to T_{SD2} .

When LED is recovered, V_{LEDN} is pulled down. If V_{LEDN} is lower than V_{LEDN_OV} , LED current limit is removed and thermal shut down threshold is changed back to T_{SD1} .

When LED is short, the external diode D_{OUT} is used to avoid LEDN overshoot which is caused by parasitic inductance of output wire.

Safety Test

The external R_{VIN} and C_{LEDN} are used to protect VIN/LEDN pin from overvoltage, especially in the ESD Test and Hipot Test, recommend $R_{VIN}=20K\Omega$ and $C_{LEDN}=100nF$.

Parallel operation application

SY22601B can be operated in parallel to support higher LED current. The circuit is shown in below.

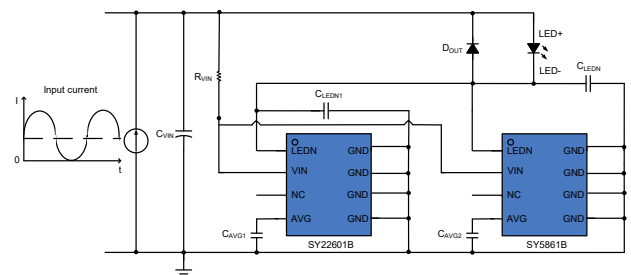
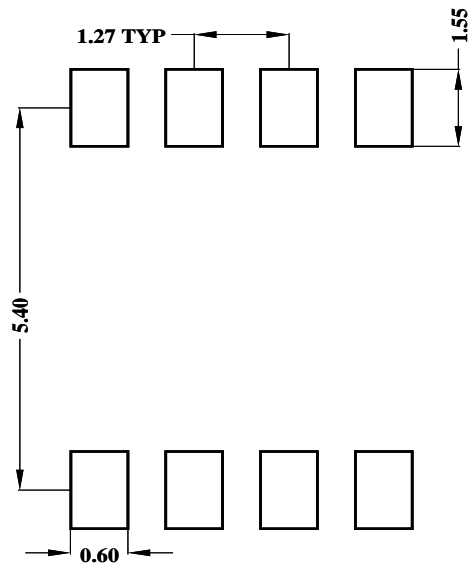
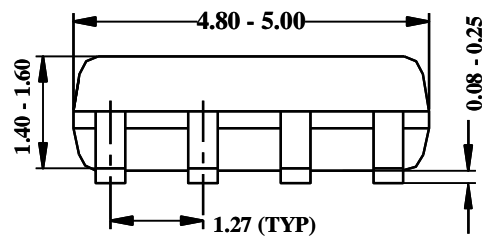
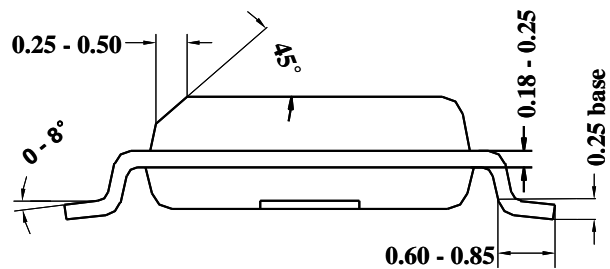
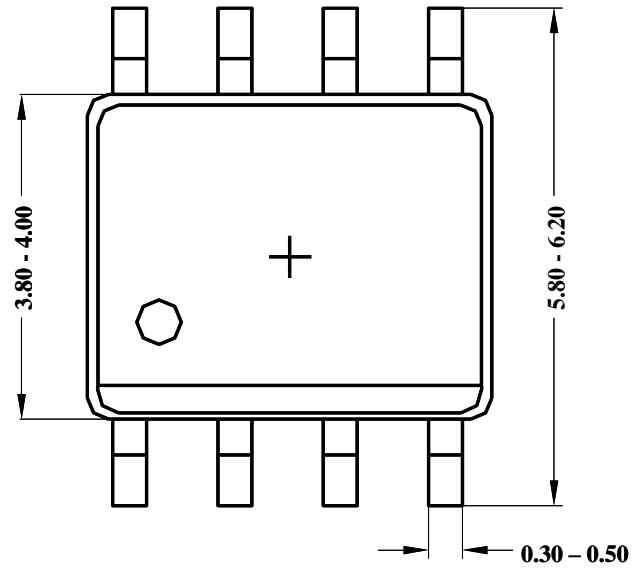


Fig3. Parallel circuit

SO8 Package Outline & PCB Layout Design



Recommended Pad Layout

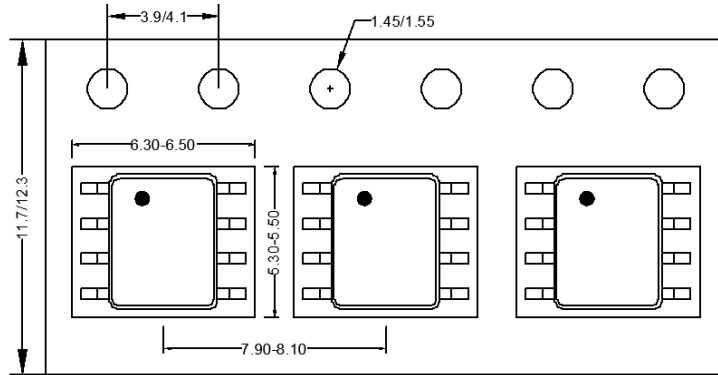


Notes: All dimensions are in millimeters.

All dimensions don't include mold flash & metal burr.

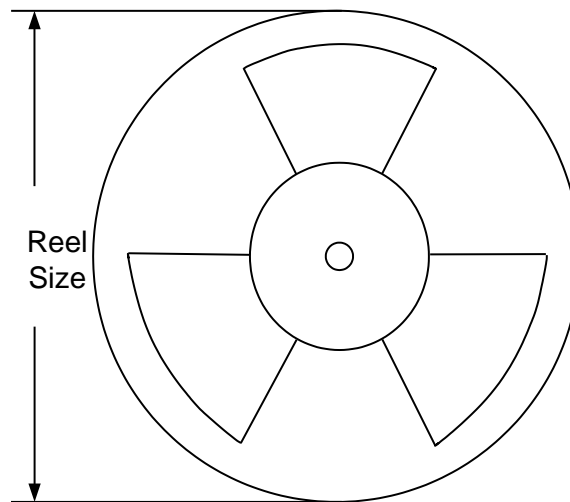
Taping & Reel Specification

1. Taping orientation for packages (SO8)



Feeding direction →

2. Carrier Tape & Reel specification for packages



Package type	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Trailer length(mm)	Leader length (mm)	Qty per reel
SO8	12	8	13"	400	400	2500

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