

SA52733 Transformer Driver for Isolated Power Supplies

General Description

The SA52733 is a 500mA, 3.3V~5V, push-pull transformer driver, designed to provide a simple solution for isolation power supplies. The IC contains the carefully designed oscillation and driving circuit to provide precise complementary signals for two N channel MOSFETs. The IC includes a soft-start feature that prevents high inrush current during power up. Its internal protection features include undervoltage lockout and thermal shutdown. The SA52733 is available in a small 5-pin SOT-23 package and qualified with AEC-Q100.

Ordering Information



Optional Spec Code

Ordering Number	Package Type	Note
SA52733AAT	SOT23-5	

Typical Application

Features

- Single 3.3V or 5V Supply
- Push-Pull Driver for Small Transformers
- High Primary-side Current Drive: 500mA
- Spread Spectrum Clocking
- Thermal Shutdown
- Soft Start
- Small 5-Pin SOT-23 Package
- AEC-Q100 Qualified

Applications

- Isolated Interface Power Supply for CAN, RS-485, RS-422, RS-232, SPI, I2C, Low-Power LAN
- Industrial Equipment
- Automobile
- Medical Equipment



Figure 1. Typical Application Circuit



Pin out (Top View)



(SOT23-5)

Top Mark: GRK*xyz* (device code: **GRK**, *x=year code*, *y=week code*, *z= lot number code*)

Pin No.	Pin Name	Description		
1	D1	Open Drain output 1. Connect this pin to one end of the transformer primary side.		
2	2 VCC Supply voltage input. Connect this pin to the center-tap of the transformer pri Buffer this voltage with a 1 μF to 10 μF ceramic capacitor.			
3	D2	Open Drain output 2. Connect this pin to another end of the transformer's primary side.		
4, 5	GND	Ground.		

Block Diagram



Figure 2. Block Diagram



Absolute Maximum Ratings (Note 1)

Supply Voltage (VCC)	
Output Switching Voltage (V _{Dx})	
Peak Output Switching Current (I _{D1} , I _{D2})	800mA
Electrostatic Discharge	
HBM (Human Body Model)	2kV
CDM (Charge Device Model)	1kV
Package Thermal Resistance (Note 2)	
θ _{JA}	240°C/W
θ _{JC}	45°C/W
Junction Temperature (T _J)	
Storage Temperature	

Recommended Operating Conditions

VCC	3V to 5.5V
V _{Dx} (VCC=5V±10%)	
V _{Dx} (VCC=3.3V±10%)	
I _{Dx}	less than 500mA
Ambient Temperature Range (T _A)	



Electrical Characteristics

$(-40^{\circ}C < T_{\Lambda} <$	・125°C 3V <vcc<5 5v<="" td=""><td>unless otherwise specif</td><td>ied All typical values a</td><td>are at T_A=25°C VCC=5V)</td></vcc<5>	unless otherwise specif	ied All typical values a	are at T _A =25°C VCC=5V)
(10 0 14	$125 \mathrm{C}, 51 \mathrm{CC} \mathrm{CS}, 51$	unicos outer wise speen.	ieu. I ill typieul vulues t	10m 1_{A} 25 $0, 100 51)$

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Voltage Supply						
Supply Current	Ivcc	$R_L=50\Omega$; $3V < VCC < 5.5V$		0.8	1.2	mA
Leakage Current on D1, D2	I _{lk}	Voltage of D1, D2=14V		0.1		μA
Output Status						
Average ON Time Mismatch between D1 and D2	tdmm	VCC=5V, R_L =50 Ω		0	50	ns
Highest spread Spectrum Oscillator Frequency	fc_H	$R_L=50\Omega$; Refer to Figure 3	314	390	467	kHz
Lowest Spread Spectrum Oscillator Frequency	fc_L	$R_L=50\Omega$; Refer to Figure 3	286	358	427	kHz
Switch on Resistance	R _{DSON}	VCC=5V, I _{D1} =I _{D2} =100mA		0.6	1.1	Ω
D1, D2 Output Rise Time	tr	VCC=5V, R_L =50 Ω ; Refer to Figure 3		75		ns
D1, D2 Output Fall Time	tf	VCC=5V, R_L =50 Ω ; Refer to Figure 3		125		ns
Break-before-make time	t _{BBM}	Refer to Figure 3		120		ns
Protection						
VCC UVLO Rising Threshold	V _{CC_uvlo_r}	VCC rising		2.7	2.9	V
VCC UVLO Failing Threshold	$V_{CC_uvlo_f}$	VCC falling	2.2	2.4		V
VCC UVLO Threshold Hysteresis	V _{CC_uvlo_hys}			300		mV
Thermal Shutdown Temperature	T _{SD}		150	165		°C
Thermal Shutdown Hysteresis	TSD_HYS			17		°C
Soft-start Time	t _{ss}	Refer to Figure 4	1			ms

Note 1: Stresses listed as the above "Absolute Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied.

Note 2: θ_{JA} and θ_{JC} are measured in the natural convection at $T_A = 25^{\circ}$ C, mounted on low effective single layer PCB in accordance with JESD51-3.

Note 3: Power dissipation and thermal limits must be observed.





Figure 3. Test Circuit for $t_{BBM},\,t_r,\,t_f,f_{C_H},\,f_{C_L}$



Figure 4. Test Circuit for t_{ss}



Figure 5. Timing Diagram







(Reference only)

Note: All dimension in millimeter and exclude mold flash & metal burr.





1. Taping Orientation

SOT23-5



Feeding direction

2. Carrier Tape & Reel Specification for Packages



Package Type	Tape Width	Pocket	Reel Size	Trailer	Leader Length	Qty per
	(mm)	Pitch(mm)	(Inch)	Length(mm)	(mm)	Reel
SOT23-5	8	4	7"	280	160	3000

3. Others: NA



Revision History

The revision history provided is for informational purpose only and is believed to be accurate, however, not warranted. Please make sure that you have the latest revision.

Revision Number	Revision Date	Description
1.0	Nov 30, 2023	Initial Release



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