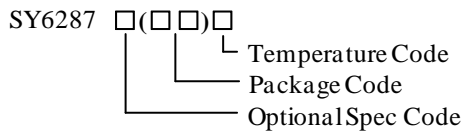


General Description

SY6287C is an ultra-low $R_{DS(ON)}$ power distribution switch with current limit to protect the power source from over current and short circuit conditions. It incorporates over temperature protection and reverse blocking function.

Ordering Information



Ordering Number	Package Type	Note
SY6287CABC	SOT23-6	
SY6287CDEC	DFN2×2-6	

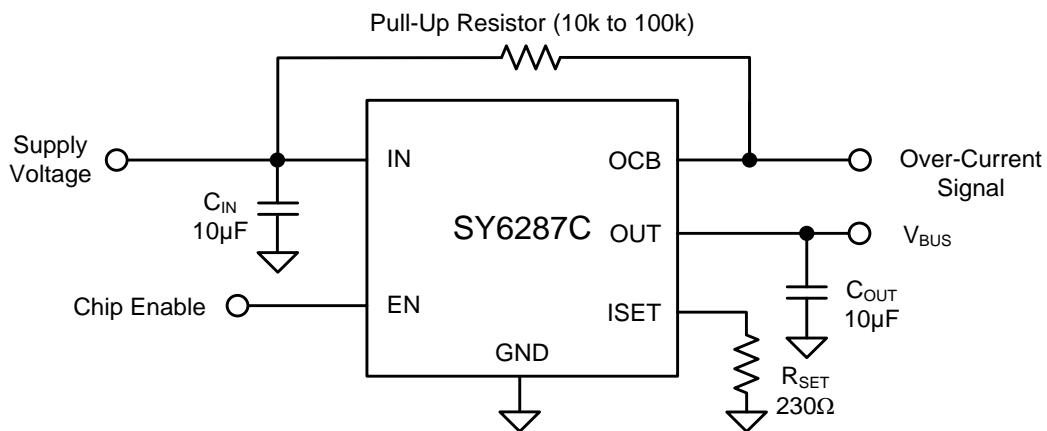
Features

- Input Voltage: 2.5V to 5.5V
- Extremely Low Power Path Resistance: 65mΩ (Typ.)
- Adjustable Current Limit Up to 2.0A
- Over Temperature Shutdown and Automatic Retry
- Reverse Blocking (No Body Diode)
- Fault Flag (OCB) Output for Over Current and Fault Conditions
- Built-in Soft-start
- Compact Package Minimizes the Board Space: SOT23-6/DFN2×2-6
- RoHS Compliant and Halogen Free
- UL(CB) Certification NO. E491480

Applications

- USB 3.1 Application
- USB 3G Data Card
- USB Dongle
- Mini PCI Accessories
- USB Charger
- Public Place Multi-USB Charger
- PC Card Hot Swap Applications

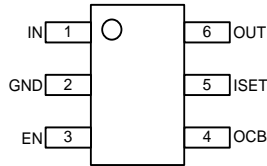
Typical Application Circuit



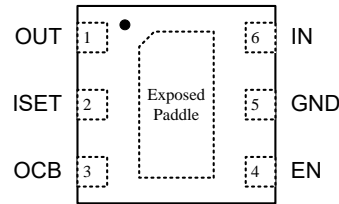
Note: If 1μF input cap will lead to large V_{in} voltage spike, it is strongly recommended to add additional 10μF ceramic cap.

Figure1. Schematic Diagram

Pinout (Top view)



SY6287CABC, SOT23-6



SY6287CDEC, DFN2x2-6

Part Number	Package Type	Top Mark ^①
SY6287CABC	SOT23-6	cLxyz
SY6287CDEC	DFN2x2-6	cMxyz

Note ①: x=year code, y=week code, z=lot number code.

Pin Name	Pin Number		Pin Description
	SOT23-6	DFN2x2-6	
IN	1	6	Input pin, decoupled with a 10μF capacitor to GND.
GND	2	5, Exposed Paddle	Ground pin.
OUT	6	1	Output pin, decoupled with a 10μF capacitor to GND.
EN	3	4	ON/OFF control, active high. Do not leave it floating.
ISET	5	2	Current limit programming pin. Connect a resistor R_{SET} from this pin to ground to program the current limit: $I_{LIM} (A) = 230/R_{SET} (\Omega)$.
OCB	4	3	Open drain fault flag.

Block Diagram

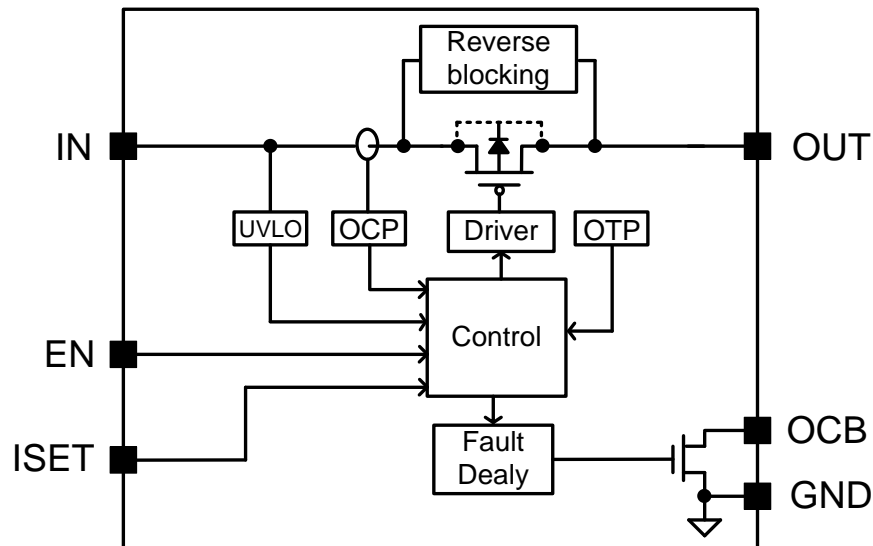


Figure2. Block Diagram



Absolute Maximum Ratings (Note 1)

IN, OUT-----	7V
ISET, OCB, EN -----	7V
Power Dissipation, P _D @ T _A = 25°C SOT23-6/DFN2×2-6 -----	0.94W/1.53W
Package Thermal Resistance (Note 2)	
SOT23-6/DFN2×2-6, θ _{JA} -----	106.4°C/W/65.3°C/W
SOT23-6/DFN2×2-6, θ _{JC} -----	41.7°C/W/16.2°C/W
Junction Temperature -----	150°C
Lead Temperature (Soldering, 10 sec.) -----	260°C
Storage Temperature Range -----	-65°C to 150°C

Recommended Operating Conditions (Note 3)

IN, OUT-----	2.5V to 5.5V
All Other Pins -----	0V to 5.5V
Junction Temperature Range -----	-40°C to 125°C
Ambient Temperature Range -----	-40°C to 85°C

Electrical Characteristics

(V_{IN} = 5V, C_{OUT}=10μF, T_A = 25°C, BOLD values indicate -40°C to 85°C, unless otherwise specified.)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage Range	V _{IN}		2.5		5.5	V
IN UVLO Threshold	V _{IN,UVLO}				2.45	V
IN UVLO Hysteresis	V _{IN,HYS}			0.1		V
Shutdown Input Current	I _{SHDN}	Open load, switch off		0.1	5	μA
		Output grounded, switch off		0.1	5	μA
Reverse Leakage Current		IN tied to GND, V _{OUT} =5V		0.1	5	μA
Quiescent Supply Current	I _Q	Open load, switch on		45	100	μA
FET R _{DS(ON)}	R _{DS(ON)}	V _{IN} =5V, I _{OUT} =0.5A		65	100	mΩ
Current Limit	I _{LIM}	V _{OUT} =4V, R _{SET} =460Ω (Note5)	0.425	0.5	0.575	A
		V _{OUT} =4V, R _{SET} =153.3Ω(Note5)	1.382	1.5	1.617	A
EN/ $\overline{\text{EN}}$ Threshold	Logic-low Voltage	V _{IL}			0.4	V
	Logic-high Voltage	V _{IH}	1.0			V
EN Input Cap	C _{EN}	(Note4)		1		pF
Output Turn On Time	t _{ON}	R _L =10Ω, C _L =1μF. Measure from EN ON to V _{OUT} reach V _{IN} ×90%	1	2	5	ms
Output Turn On Rise Time	t _R	R _L =10Ω, C _L =1μF. Measure from V _{OUT} =10% of V _{IN} to 90% of V _{IN}	1	2	5	ms
Output Turn Off Time	t _{OFF}	R _L =10Ω, C _L =1μF. Measure from EN OFF to V _{OUT} reach V _{IN} ×10%		22		μs
Output Turn Off Fall Time	t _F	R _L =10Ω, C _L =1μF. Measure from V _{OUT} =90% of V _{IN} to 10% of V _{IN}		21		μs
OCB Low Resistance	R _{OCB}	V _{IN} =5V, I _L =10μA		9		Ω
		V _{IN} =3.3V, I _L =10μA		12		Ω



SY6287C

OCB Leakage Current	I_{LKG_OCB}	$V_{OCB}=5V$		0.01	1	μA
Thermal Shutdown Temperature	T_{SD}			150		$^{\circ}C$
Thermal Shutdown Hysteresis	T_{HYS}			20		$^{\circ}C$
Current-limit Response Time	t_{OC_res}	$V_{OUT}=0V$ (Note5)		25		μs
Over Current Flag Response Time	t_{OCB}	$V_{OUT}=V_{IN}/2$ to FLG low	4	8	12	μs

Note 1: Stresses beyond the “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

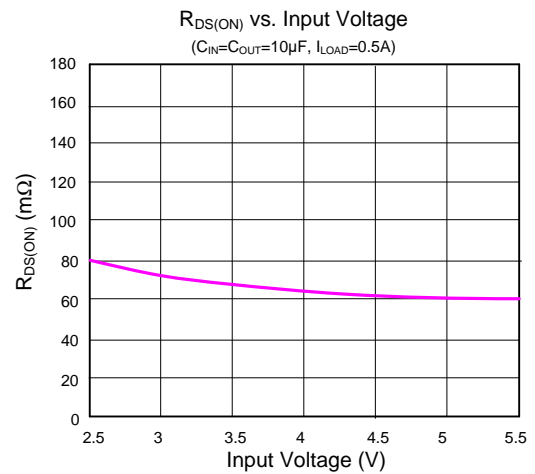
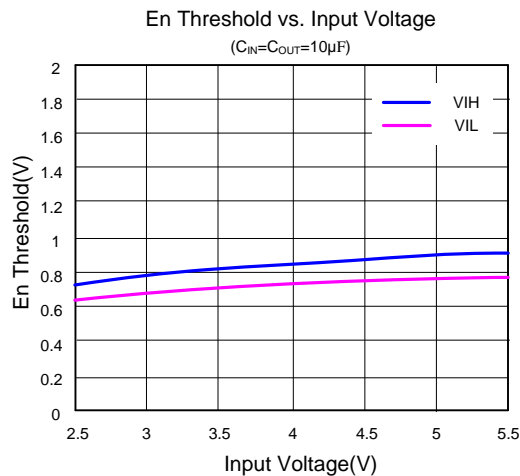
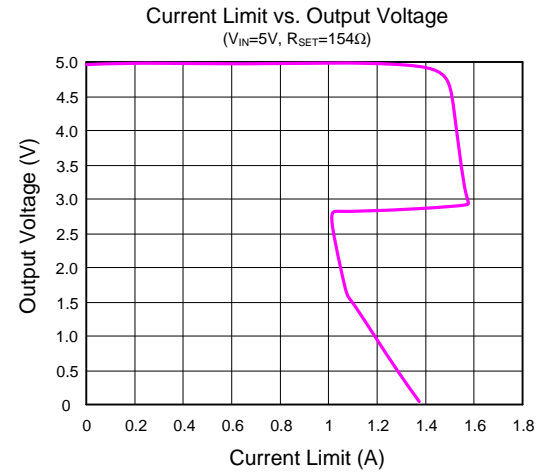
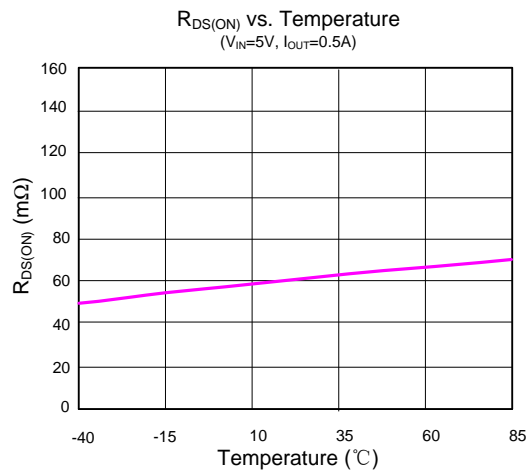
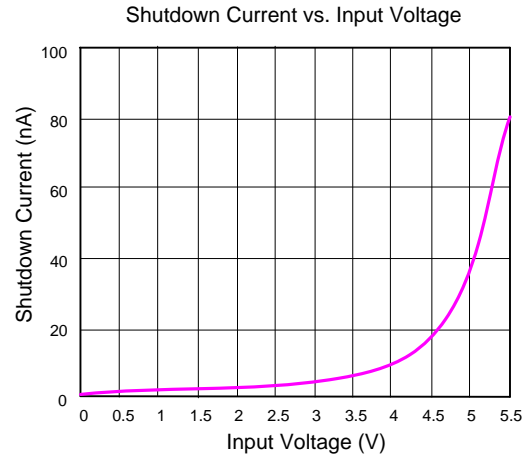
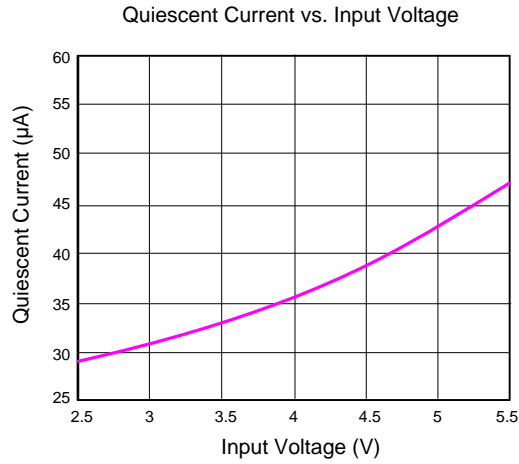
Note 2: θ_{JA} is measured in the natural convection at $T_A = 25^{\circ}C$ on a Silergy’s test board. Pin 2 of SOT23-6 packages is the case position for θ_{JC} measurement.

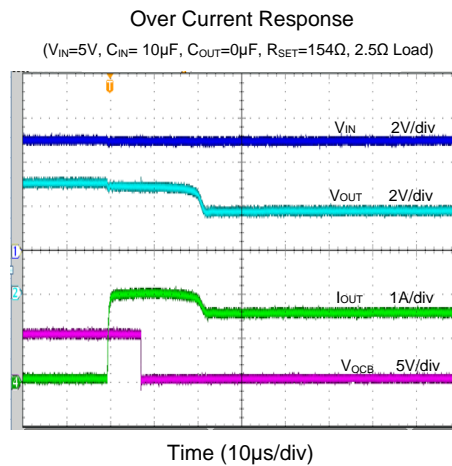
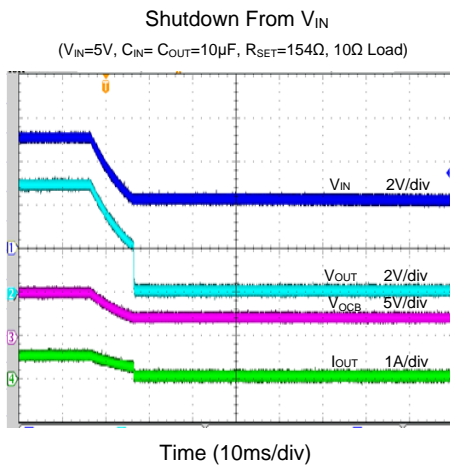
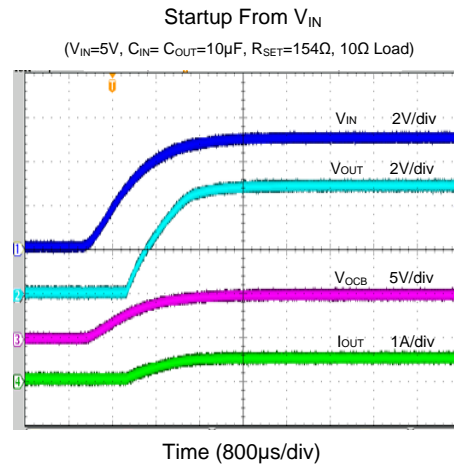
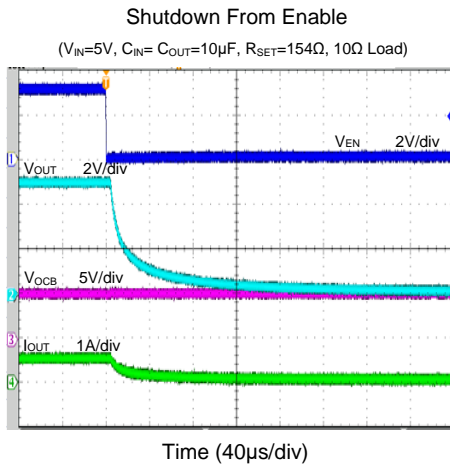
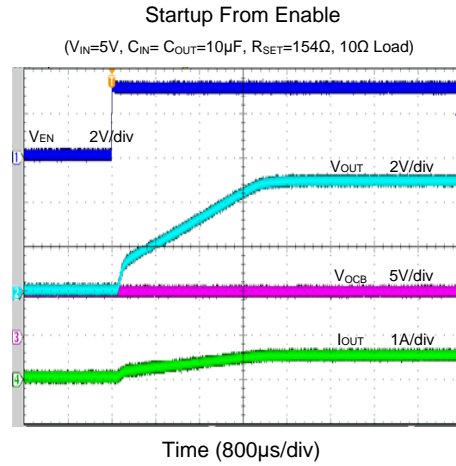
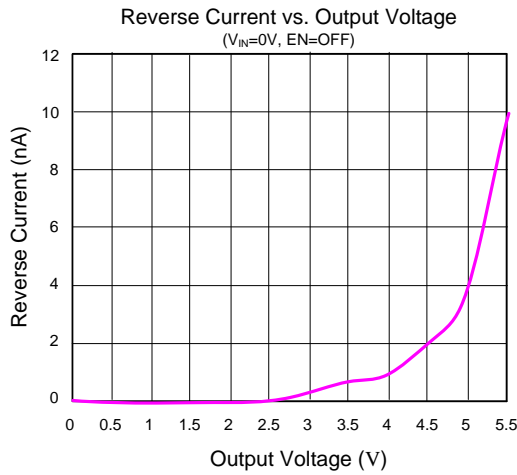
Note 3: The device is not guaranteed to function outside its operating conditions.

Note 4: Guaranteed by design but not production tested.

Note5: Current limit threshold is determined by $I_{LMT}=230V/R_{SET}$, where R_{SET} is in ohms.

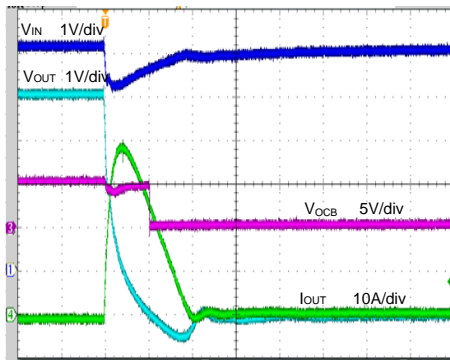
Typical Performance Characteristic





Short Circuit Response

($V_{IN}=5V$, $C_{IN}=C_{OUT}=10\mu F$)



Time (10µs/div)

Operation

The SY6287C is a current limited P-channel MOSFET power switch designed for USB load-switching or hot plug applications. It incorporates the over temperature protection and reverse blocking function, so the IC prevents current flow from OUT to IN when out being externally forced to a higher voltage than IN.

Over Current Protection

The SY6287C supports Current limit programming. Connect a resistor R_{SET} from the ISET pin to ground to program the current limit:

$$I_{LIM} (A) = 230 / R_{SET} (\Omega)$$

The minimum current limit is 0.1A. Current limit beyond 2.0A is not recommended.

When the over current condition is sensed, the gate of the pass switch is modulated to achieve constant output current. If the over current condition persists for a long time, the junction temperature may exceed 150°C, and over-temperature protection will shut down the part. Once the chip temperature drops below 130°C, the part will restart.

Table1. Current Limit vs. R_{SET}

$R_{SET}(\Omega)$	Current Limit Threshold(mA)		
	MIN	TYP	MAX
460.0	425	500	575
153.3	1380	1500	1620

Fault Flag(OCB)

The OCB output is asserted (active low) when Thermal shutdown protection is triggered or over current condition persists for 8 μ s. The output remains asserted until fault condition is removed. Connecting a heavy capacitance load to an enabled device can cause a momentary over current condition; However, no false reporting on OCB occurs due to 8 μ s deglitch circuit.

Supply Filter Capacitor

In order to prevent the input voltage drooping during hot-plug events, a 10 μ F ceramic capacitor from VIN to GND is strongly recommended. However, higher capacitor values could reduce the voltage droop on the input further. Furthermore, an output short will cause ringing on the input without the input capacitor. It could destroy the internal circuitry when the input transient exceeds the absolute maximum supply voltage even for a short duration.

Output Filter Capacitor

A 10 μ F output ceramic capacitor is recommended to be placed close to the IC and output connector to reduce voltage drop during load transient. Higher values of output capacitor can be used to further reduce the drop during high current application.

Reverse Block Function:

The SY6287C integrates reverse block function. Once the deviation voltage of OUT-IN exceeds 100mV, the reverse block function is triggered. The power FET will be shutdown in 700ns block the reverse current flow from OUT to IN.

PCB Layout Guide

For best performance of the SY6287C, the following guidelines must be strictly followed:

- 1) Keep all VBUS traces as short and wide as possible and use at least 2 ounce copper for all VBUS traces.
- 2) Locate the output capacitor as close to the connectors as possible to lower impedance (mainly inductance) between the port and the capacitor and improve transient performance.
- 3) Input and output capacitors should be placed closed to the IC and connected to ground plane to reduce noise coupling.

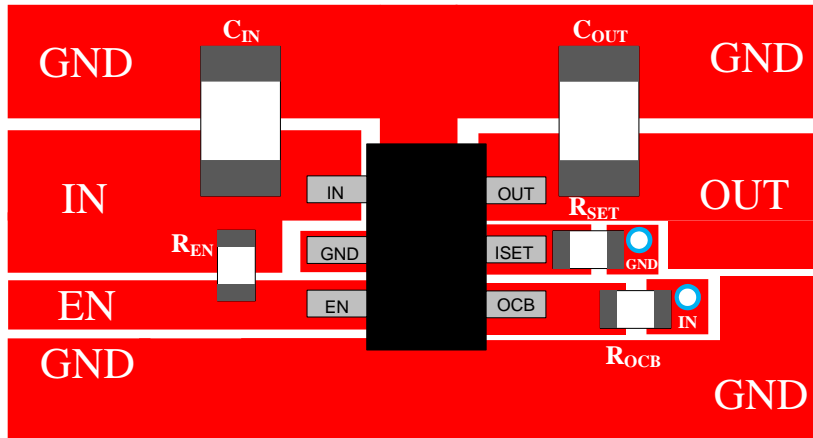


Figure3. SY6287CABC PCB Layout Suggestion

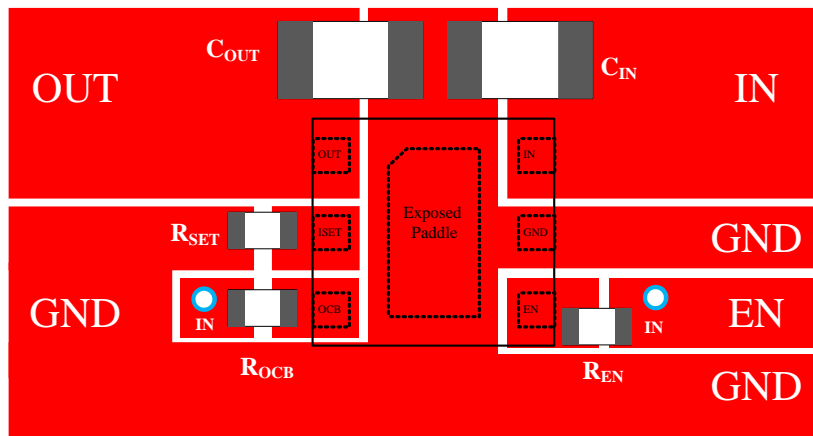
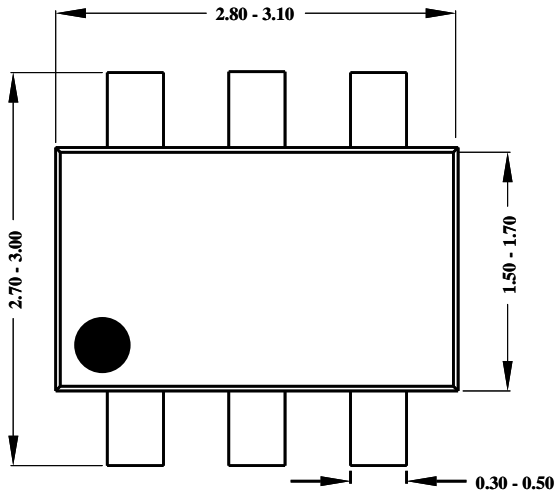
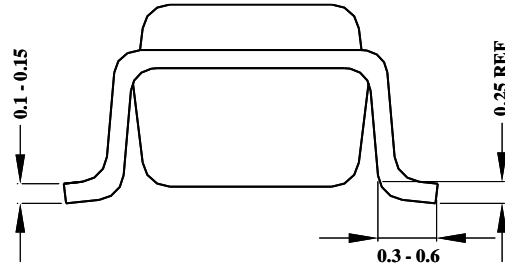


Figure4. SY6287CDEC PCB Layout Suggestion

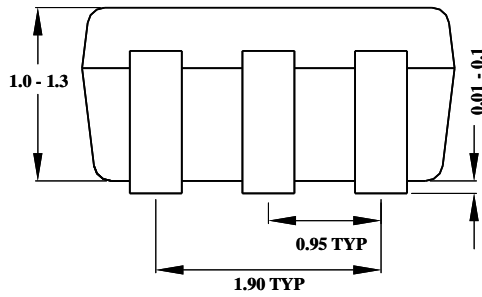
SOT23-6 Package Outline & PCB Layout



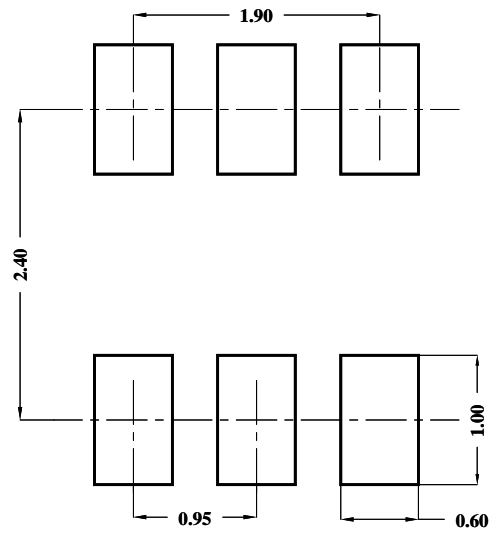
Top View



Side View A



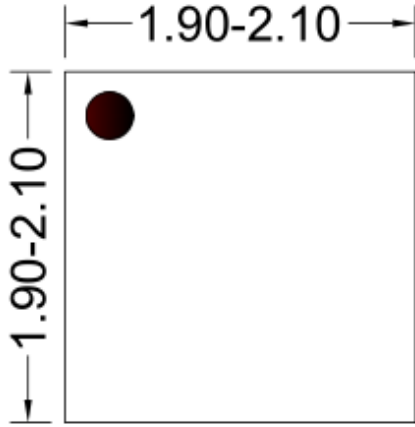
Side View B



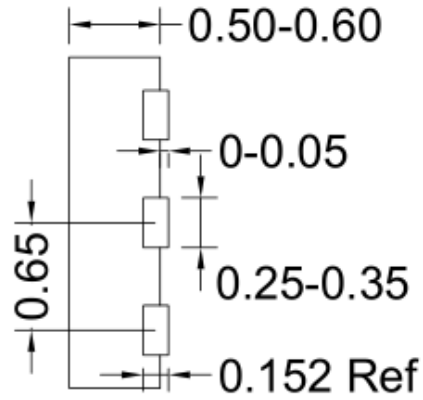
Recommended Pad Layout

Notes: **All dimension in millimeter.**
 All dimension don't include mold flash & metal burr.

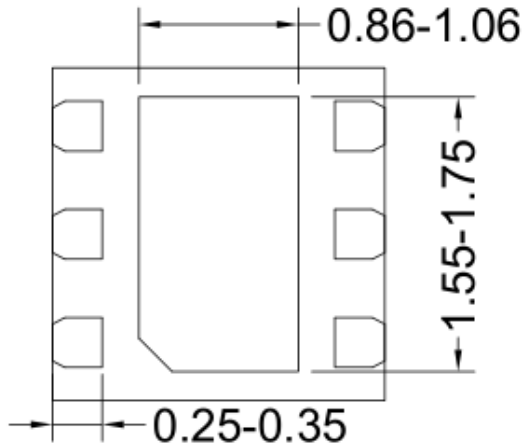
DFN2×2-6 Package Outline



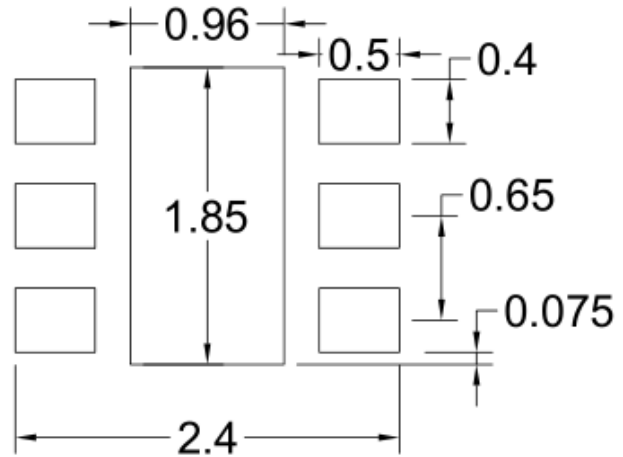
Top View



Side View



Bottom View



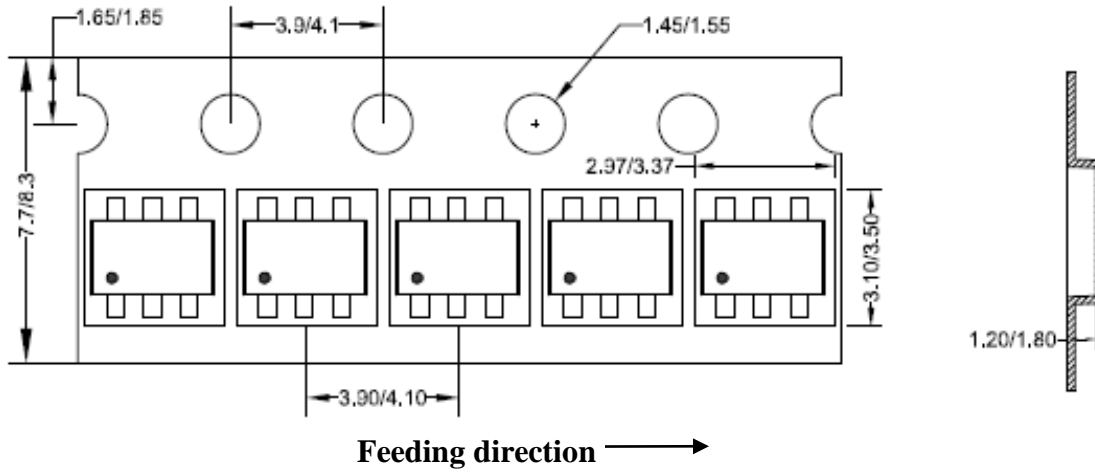
Recommended PCB layout

Notes: All dimension in millimeter and exclude mold flash & metal burr

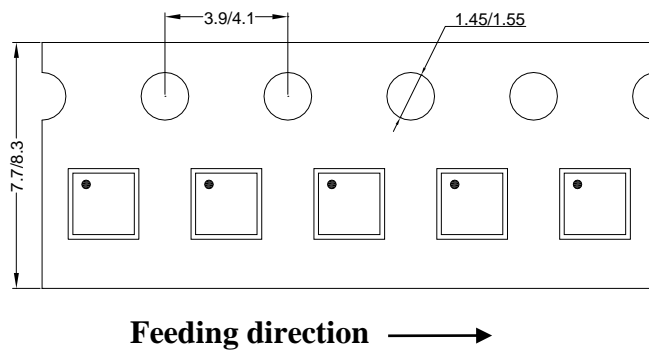
Taping & Reel Specification

1. Taping Orientation

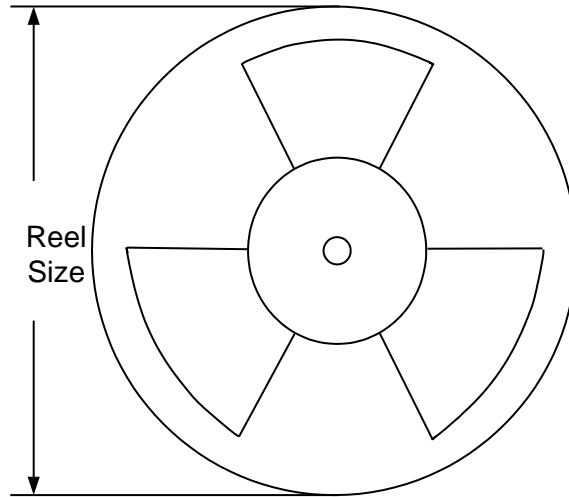
SOT23-6



DFN2x2 taping orientation



2. Carrier Tape & Reel Specification for Packages

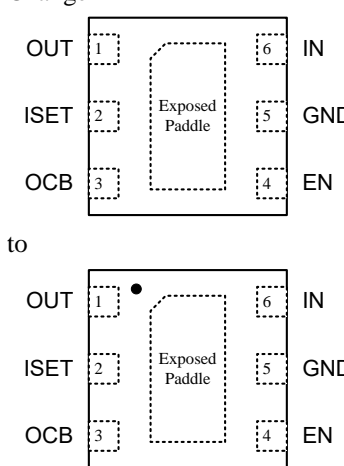
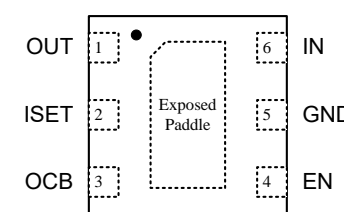


Package types	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Trailer length(mm)	Leader length (mm)	Qty per reel
SOT23-6	8	4	7"	280	160	3000
DFN2x2	8	4	7"	400	160	3000

3. Others: NA

Revision History

The revision history provided is for informational purpose only and is believed to be accurate, however, not warranted. Please make sure that you have the latest revision.

Date	Revision	Change																								
Apr 07, 2020	Revision 0.9D	The minimum current limit changes from 0.2A to 0.1A in Over Current Protection Operation (page 8)																								
Oct 18, 2019	Revision 0.9C	Add “RoHS Compliant and Halogen Free” & “UL(CB) Certification NO. E491480” in Features (page1)																								
Jan 31, 2018	Revision 0.9B	<p>EC table Change</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td rowspan="2" style="width: 30%;">Current Limit Factor</td> <td rowspan="2" style="width: 10%;"></td> <td style="width: 15%;">$I_{OUT}=0.5A, V_{OUT}=4V$ (Note5)</td> <td style="width: 5%;">195.5</td> <td style="width: 5%;">230</td> <td style="width: 5%;">264.5</td> <td style="width: 5%;">V</td> </tr> <tr> <td>$I_{OUT}=1.5A, V_{OUT}=4V$ (Note5)</td> <td>212</td> <td>230</td> <td>248</td> <td>V</td> </tr> </table> <p>to</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td rowspan="2" style="width: 30%;">Current Limit</td> <td rowspan="2" style="width: 10%;">I_{LM}</td> <td style="width: 15%;">$V_{OUT}=4V, R_{SET}=460\Omega$ (Note5)</td> <td style="width: 5%;">0.425</td> <td style="width: 5%;">0.5</td> <td style="width: 5%;">0.575</td> <td style="width: 5%;">A</td> </tr> <tr> <td>$V_{OUT}=4V, R_{SET}=153.3\Omega$(Note5)</td> <td>1.382</td> <td>1.5</td> <td>1.617</td> <td>A</td> </tr> </table>	Current Limit Factor		$I_{OUT}=0.5A, V_{OUT}=4V$ (Note5)	195.5	230	264.5	V	$I_{OUT}=1.5A, V_{OUT}=4V$ (Note5)	212	230	248	V	Current Limit	I_{LM}	$V_{OUT}=4V, R_{SET}=460\Omega$ (Note5)	0.425	0.5	0.575	A	$V_{OUT}=4V, R_{SET}=153.3\Omega$ (Note5)	1.382	1.5	1.617	A
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		$V_{OUT}=4V, R_{SET}=153.3\Omega$ (Note5)	1.382	1.5	1.617	A																				
Jan 29, 2018	Revision 0.9A	<p>1.Pinout(page2) Change</p>  <p>to</p>  <p>2. Add Revision History. Page14.</p>																								
Jan 19, 2018	Revision 0.9	Initial Release																								



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