



Low Capacitance TVS Protection

Features

Transient Protection for High-speed Data Lines
 IEC 61000-4-2 (ESD) ±30kV(Air)
 ±30kV(Contact)

±30K V (Contac

IEC 61000-4-5 (Surge) 10A (8/20 μs)

- For 5.0V and Below the Operating Voltage
- Package Optimized for High-speed lines
- Ultra-small Package: DFN0.6×0.3-2

DFN1.0×0.6-2

- Protects One Data, Control or Power Line
- Low Capacitance: 2.0pF (Typical)
- Low Leakage Current: 0.1μA @ V_{RWM} (Typical)
- Low Clamping Voltage
- Each I/O Pin can Withstand Over 1000 ESD Strikes for ±8kv Contact Discharge

Description

The SYT11L05 is a low-capacitance transient voltage suppressor (TVS) designed to provide the electrostatic discharge (ESD) protection for high-speed data interfaces. With typical capacitance of 2.0pF, the SYT11L05 is designed to protect parasitic-sensitive systems against over-voltage and over-current transient events. It complies with IEC61000-4-2 (ESD) (±30kV air, ±30kV contact discharge), IEC61000-4-5 (Surge) (10A, 8/20μs), etc.

Each SYT11L05 device can protect one data line. It offers the system designers flexibility to protect the single data line where the space is a premium concern.

Applications

- USB2.0
- Portable Electronics
- Desktops, Servers and Notebooks
- Cellular Phones
- MP3 Ports
- Digital Camera Ports

Mechanical Characteristics

• Package: DFN0.6×0.3-2

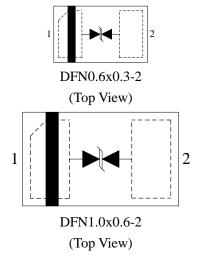
DFN1.0×0.6-2

- Marking: Device code, date
- Packaging: Tape and Reel

Circuit Diagram



Pin Configuration



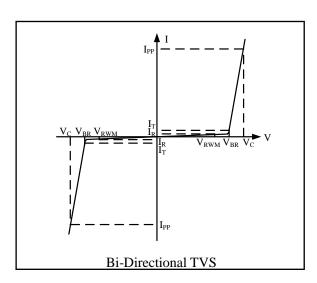


Absolute Maximum Rating

Symbol	Parameter	Value	Units
V_{ESD}	ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	±30 ±30	kV
I_{PP}	I _{PP} Peak Pulse Current (8/20μs) 10		A
P_{PK}	Peak Pulse Power (8/20μs)	100	W
T_{OPT}	Operating Temperature	-40/+125	°C
${ m T_{STG}}$	Storage Temperature	-55/+150	°C

Electrical Characteristics (T = 25°C)

Symbol	Parameter	
V_{RWM}	Nominal Reverse Working Voltage	
I_R	Reverse Leakage Current @ V _{RWM}	
V_{BR}	Reverse Breakdown Voltage @ I _T	
I_{T}	Test Current for Reverse Breakdown	
$V_{\rm C}$	Clamping Voltage @ IPP	
I_{PP}	Maximum Peak Pulse Current	
C _{ESD}	Parasitic Capacitance	
V_R	Reverse Voltage	
f	Small Signal Frequency	



Symbol	Test Condition	Minimum	Typical	Maximum	Units
V_{RWM}				5.0	V
I_R	$V_{RWM} = 5.0V, T = 25^{\circ}C$		0.01	0.1	μΑ
V_{BR}	$I_T = 1 \text{mA}$	6.0			V
V_C^1	$I_{PP} = 10A, t_p = 8/20 \mu s$		9.5		V
$V_{\rm C}{}^{1}$	$I_{PP} = 16A, t_p = 10/100ns$		9		V
$R_{\mathrm{DYN}}^{1,2}$	$t_p = 10/100 ns$		0.17		Ω
C _{ESD} ¹	$V_R = 0V, f = 1MHz$		2.0	3.0	pF

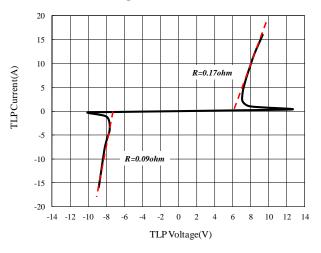
NOTES

¹Guaranteed by design and not subject to production test.

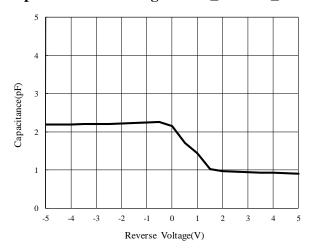
 $^{^2}R_{\rm DYN}$ calculated based on Ipp=8A to Ipp=16A, t_p = 10/100ns.



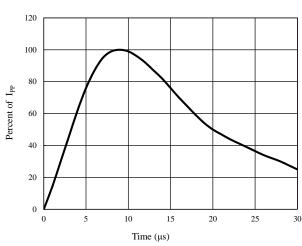
TLP Testing of I/O_1 to I/O_2



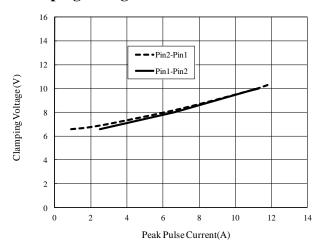
Capacitance vs. Voltage of I/O_1 to I/O_2



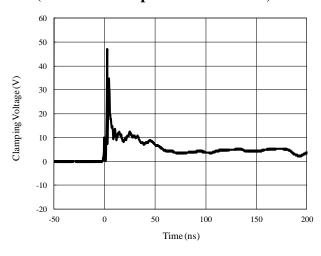
Pulse Waveform



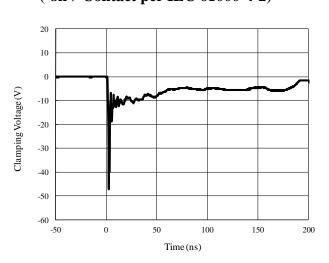
Clamping Voltage vs. Peak Pulse Current



ESD Clamping of I/O_1 to I/O_2 (+8kV Contact per IEC 61000-4-2)



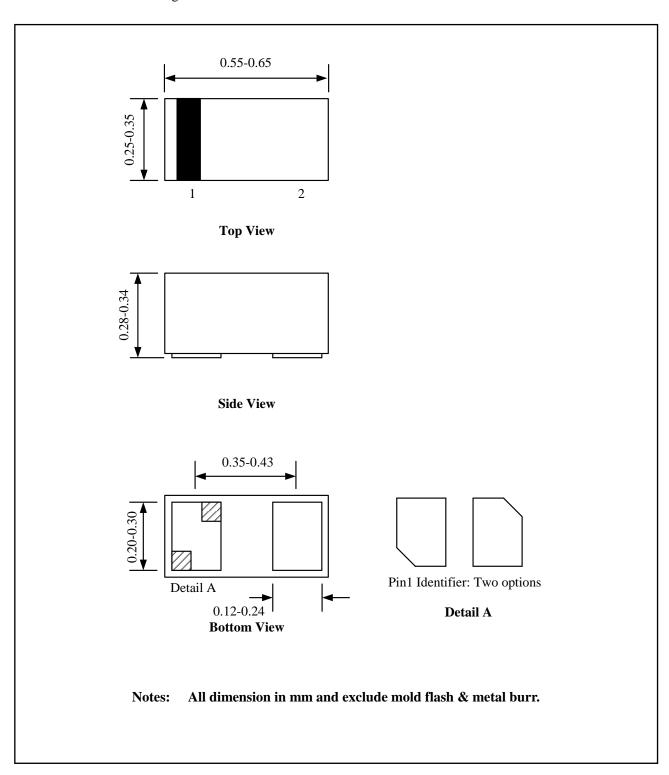
ESD Clamping of I/O_1 to I/O_2 (-8kV Contact per IEC 61000-4-2)





Package Outline

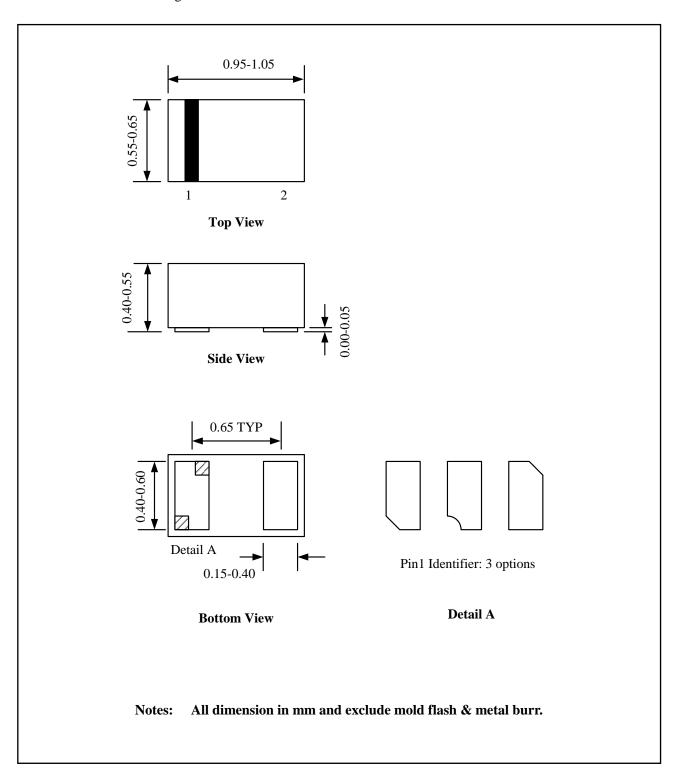
• DFN0.6*0.3-2 Package





Package Outline

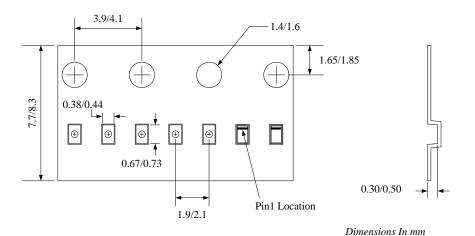
• DFN1.0*0.6-2 Package





Tape and Reel Specification

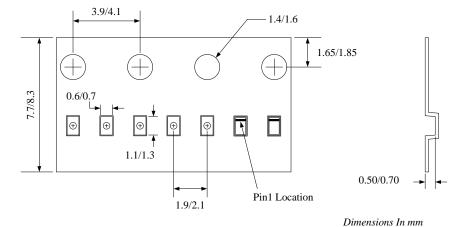
• DFN0.6*0.3-2



Feeding direction —

Package types	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Qty per reel (pcs)
DFN0.6*0.3-2	8	2	7"	10000

• DFN1.0*0.6-2



Feeding direction -

Package types	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Qty per reel (pcs)
DFN1.0*0.6-2	8	2	7"	10000



Marking Codes



DFN0.6*0.3-2



DFN1.0*0.6-2

Note:

- (1) "W", "Y" is device code, fixed.
- (2) "M" is date code.

Ordering Information

Part Number	Package	QTY/Reel
SYT11L05DXC	DFN0.6*0.3-2	10,000
SYT11L05DWC	DFN1.0*0.6-2	10,000





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