



Low Capacitance ESD/Surge Protection for Gigabit Ethernet Interfaces

Features

• Transient protection for high-speed data lines

IEC 61000-4-2 (ESD) ±30kV (Air)

±30kV (Contact)

IEC 61000-4-4 (EFT) 40A (5/50 ns)

IEC 61000-4-5 (Surge) 25A (8/20μs)

- Package optimized for high-speed lines
- Provides protection for two line pairs
- Low capacitance: 3.8pF @ 0V (Typical)
- Low leakage current: 0.1μA @ V_{RWM} (Typical)
- Low operating and clamping voltage
- Each I/O pin can withstand over 1000 ESD strikes for ±8kV contact discharge

Description

SYS11L03SEC is a low-capacitance Transient Voltage Suppressor (TVS) array designed to provide electrostatic discharge (ESD) protection for high-speed data interfaces. With typical capacitance of 3.8pF only, SYS11L03SEC is designed to protect parasitic-sensitive systems against over-voltage and over-current transient events. It complies with IEC 61000-4-2 (ESD), ±30kV air, ±30kV contact discharge, IEC 61000-4-4 (electrical fast transient - EFT) (40A, 5/50 ns), IEC 61000-4-5 (Surge) (25A, 8/20μs), very fast charged device model (CDM) ESD and cable discharge event (CDE), etc.

SYS11L03SEC is in a DFN2.5×2.5-10 package. Each SYS11L03SEC device can protect two high-speed line pairs. The combined features of low capacitance and high ESD robustness make SYS11L03SEC ideal for high-speed data port and high-frequency line (e.g., Gigabit Ethernet Ports) applications. The low clamping voltage of the SYS11L03SEC guarantees a minimum stress on the protected IC.

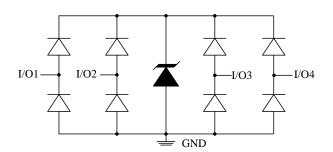
Applications

- 10/100/1000M Ethernet Ports
- WAN/LAN Equipment
- Desktops, Servers and Notebooks
- Cellular Phones
- Switching Systems
- Audio/Video Inputs

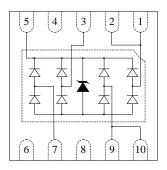
Mechanical Characteristics

- DFN2.5×2.5-10 package
- Flammability Rating: UL 94V-0
- Marking: Part number, Date
- Packaging: Tape and Reel

Circuit Diagram



Pin Configuration



DFN2.5×2.5-10 (Top View)

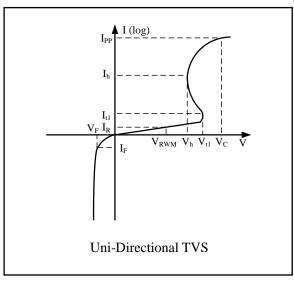


Absolute Maximum Rating

Symbol	Parameter	Value	Units
I_{PP}	Peak Pulse Current (8/20µs)	25	A
P_{PK}	Peak Pulse Power (8/20μs)	450	Watts
V_{ESD}	ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	±30 ±30	kV
T_{OPT}	Operating Temperature	-55 to +125	°C
$T_{ m STG}$	Storage Temperature	-55 to +150	°C

Electrical Characteristics $(T = 25^{\circ}C)$

Symbol	Parameter	
V_{RWM}	Nominal Reverse Working Voltage	
I_R	Reverse Leakage Current @ V _{RWM}	
V_{t1}	Trigger Voltage	
I_{t1}	Trigger Current @ V _{t1}	
V_h	Holding Voltage	
I_h	Holding Current @ V _h	
$V_{\rm C}$	Clamping Voltage @ I _{PP}	
I_{PP}	Maximum Peak Pulse Current	
V_{F}	Forward Voltage @ I _F	
C_{ESD}	Parasitic Capacitance	



Symbol	Test Condition	Minimum	Typical	Maximum	Units
V_{RWM}				3.3	V
I_R	$V_{RWM} = 3.3V, T = 25^{\circ}C$		0.1	1.0	μΑ
V_{h}	$I_h = 10 \text{mA}$	3.3		4.5	V
V_{C}^{1}	$I_{PP} = 1A$, $t_p = 8/20 \mu s$ (Each Line)			5.5	V
V_C^1	$I_{PP}=10A,t_p=8/20\mu s$ (Each Line)			8.5	V
V_C^1	$I_{PP}=25A,t_p=8/20\mu s$ (Each Line)			16.0	V
C _{ESD} ¹	Between I/O Pins and Ground $V_R = 0V$, $f = 1MHz$		3.8	5.0	pF
C _{ESD} ¹	Between I/O Pins $V_R = 0V$, $f = 1MHz$		2.0	2.5	pF

NOTES

¹Guaranteed by design and not subject to production test.

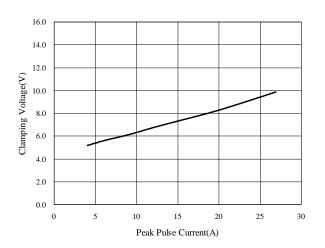




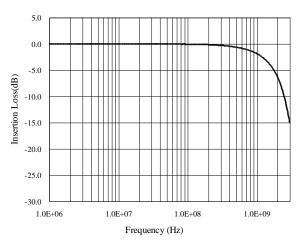
8/20μs Pulse Waveform

1.1 1.0 0.9 0.8 Dercent of Ipp 0.7 0.6 0.5 0.4 0.4 0.3 0.2 0.1 0.0 0 5 10 25 Time (us)

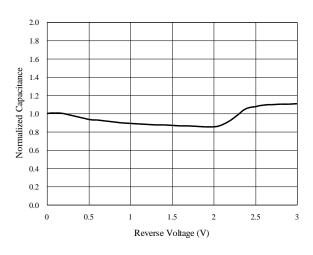
Clamping Voltage Vc vs. Current IPP



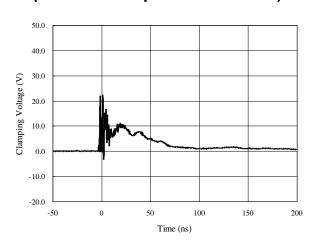
Insertion Loss S21



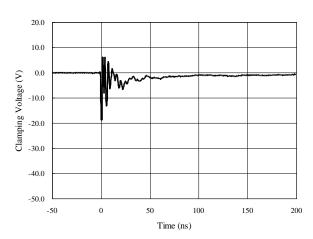
Normalized Capacitance vs. Voltage



ESD Clamping of I/O to GND (+8kV Contact per IEC 61000-4-2)



ESD Clamping of I/O to GND (-8kV Contact per IEC 61000-4-2)

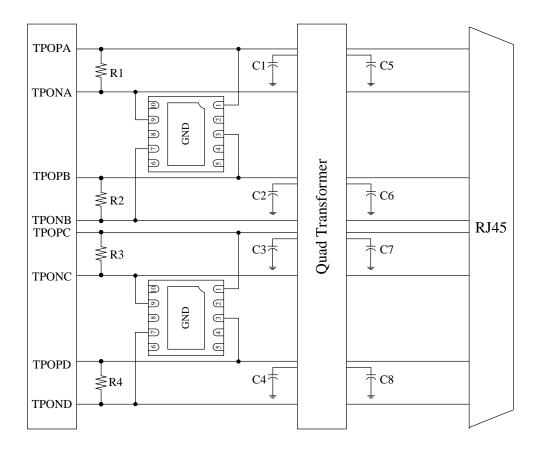




Application Information

Electronic equipment is susceptible to damage caused by a variety of sources, including Electrostatic Discharge (ESD), Electrical Fast Transients (EFT) and Lightning strikes. The SYS11L03SEC was designed to protect the sensitive equipment from damage which may be induced by such transient events. This product can be configured in different connections to meet the requirement of common-mode and differential-mode as follows:

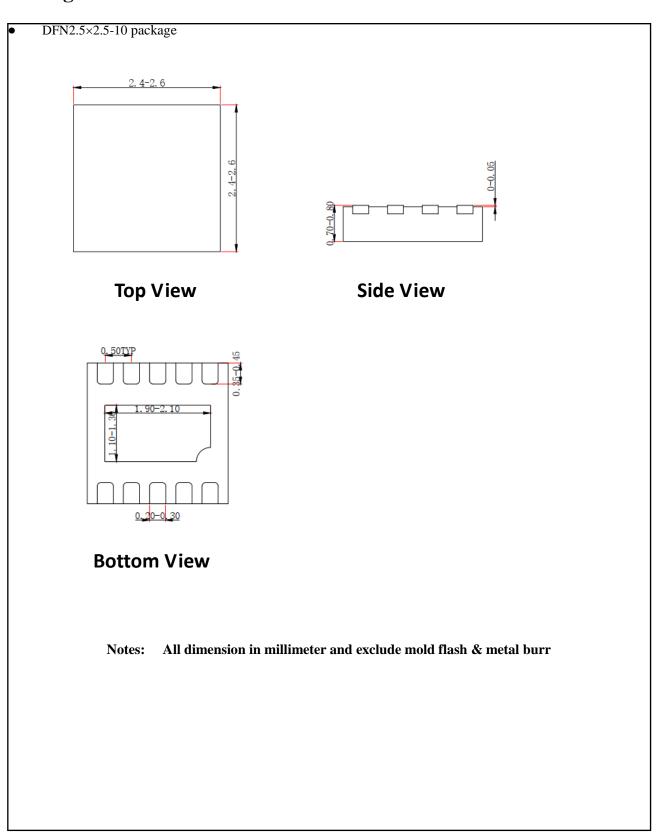
Gigabit Ethernet Protection



Schematic Diagram for Gigabit Ethernet ESD/Surge Protection using SYS11L03SEC



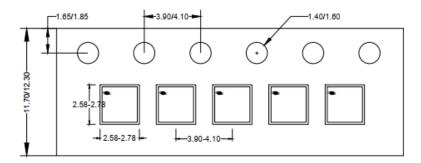
Package Outline





Tape and Reel Specification

DFN2.5×2.5-10





Feeding direction _____

Package types	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Qty per reel (pcs)
DFN2.5x2.5-10	12	4	7''	3000

Marking Codes



Ordering Information

Part Number	Working Voltage	Quantity Per Reel	Reel Size
SYS11L03SEC	3.3V	3,000	7 Inch

Note:

- (1) "2201" is the part number, fixed.
- (2) "YYWW" is date code. "YY" is year (2011 is "11"); while "WW" is assembly week in a year.



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