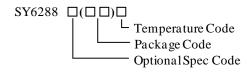


Low Loss Power Distribution Switch

General Description

SY6288C7/D7 is an ultra-low R_{DS(ON)} switch with current limiting function to protect the power source from over current and short circuit conditions.

Ordering Information

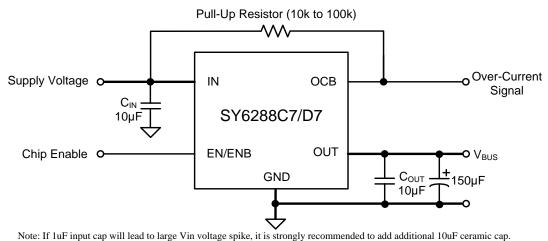


Ordering Number	Package Type
SY6288C7AAC	SOT23-5
SY6288D7AAC	SOT23-5

Features

- Distribution Voltages: 2.5V to 5.5V
- Over Temperature Shutdown and Automatic Retry
- Reverse Blocking (No Body Diode)
- At Shutdown, OUT Can be Forced Higher Than IN
- Fault Flag (OCB) Output if Over Current, Thermal Shut Down, Reverse Blocking Happens
- Automatic Output Discharge at Shutdown
- **Built-in Soft-start**
- 0.4ms Rise Time
- RoHS Compliant and Halogen Free
- Two Enable Polarities
 - SY6288C7: Active High/2.5A
 - SY6288D7: Active Low/2.5A
- Compact Packages Minimize Board Space: SOT23-5
- UL Certification NO. E491480

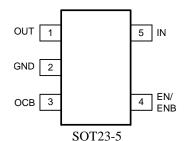
Typical Application Circuit



Note: A low-ESR 150µF aluminum electrolytic or tantalum between OUT and GND is strongly recommended.



Pinout(Top View)



Part Number	Package type	Top Mark [®]
SY6288C7AAC	SOT23-5	Pt xyz
SY6288D7AAC	SOT23-5	Pw xyz

Note ①: $x=year\ code$, $y=week\ code$, $z=lot\ number\ code$.

Functional Pin Description

Pin Name	Pin Number (SOT23-5)	Pin Description
IN	5	Input pin.
GND	2	Ground pin.
OUT	1	Output pin.
EN- SY6288C7 ENB- SY6288D7	4	ON/OFF control. Do not leave it float. EN: high enable. ENB: low enable.
OCB	3	Open drain fault flag.

Absolute Maximum Ratings (Note 1)

IN, OUT, OCB	
EN/ENB	0.3V to $V_{IN}+0.3V$
Power Dissipation, PD @ TA = 25°C SOT23-5	0.6W
Package Thermal Resistance (Note 2)	
SOT23-5, θ JA	100°C/W
SOT23-5, θ JC	30°C/W
Junction Temperature Range	
Lead Temperature (Soldering, 10 sec.)	260°C
Storage Temperature Range	

Recommended Operating Conditions (Note 3)

IN	2.5 V to 5.5 V
EN/ENB	$0V$ to $V_{\rm IN}$
OUT, OCB	0 to 5.5V
Junction Temperature Range	
Ambient Temperature Range	40°C to 85°C



Electrical Characteristics

 $(V_{IN} = 5V, C_L = 1\mu F, per channel, T_A = 25^{\circ}C unless otherwise specified)$

Parameter Symbol		Symbol	Test Conditions	Min	Тур	Max	Unit
Input Voltage Range		V_{IN}		2.5		5.5	V
Shutdown Input Current		Ishdn	Open load, switch off		0.1	1	μA
			Output grounded, switch off		0.1	1	μA
Quiescent Supply	y Current	I_Q	Open load, switch on		32		μA
FET RON		R _{DS(ON)}		50	63	75	m Ω
Current Limit		I_{LIM}		2.7	3.55	4.4	A
Short Circuit Output Current		Ios	SY6288C7/D7, OUT connected to GND device enabled	1.2	1.8	2.4	A
EN/ EN	Logic-Low Voltage	V_{IL}				0.8	V
Threshold	Logic-High Voltage	V_{IH}		1.75			V
IN UVLO Thresh	hold	V _{IN,UVLO} V _{IN,HYS}				2.4	V
IN UVLO Hyste	IN UVLO Hysteresis				0.1		V
Turn-ON Time		ton	$R_L=5 \Omega, C_L=1\mu F$		400		μs
OCB Low Resist	OCB Low Resistance				10		Ω
OCB Delay Time		tocb_Delay			10		ms
OUT Shutdown Discharge		R_{DIS}			10		Ω
Resistance		NDIS			10		22
Thermal Shutdown		T_{SD}			150		°C
Temperature		1 20					
Thermal Shutdown Hysteresis					20		°C

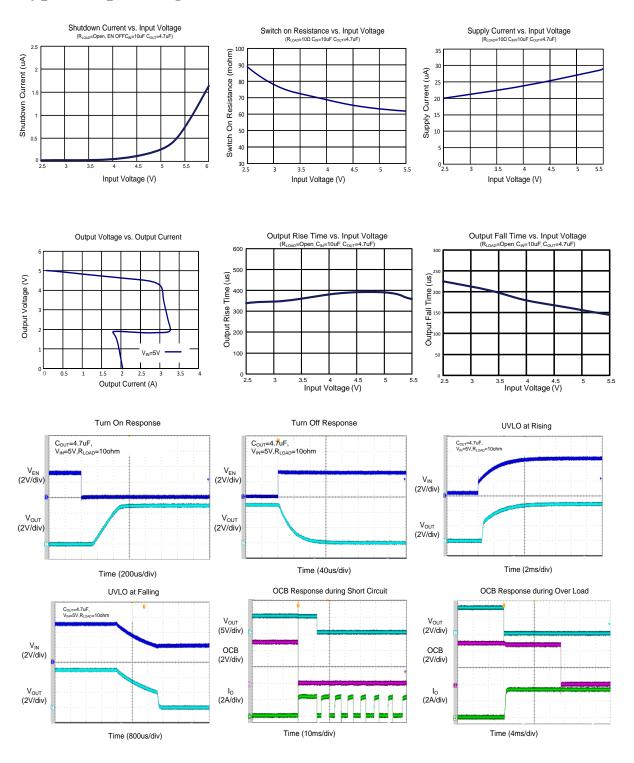
Note 1: Stresses beyond the "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note 2: θ_{JA} is measured in the natural convection at $T_A = 25$ °C on Silergy evaluation board.

Note 3: The device is not guaranteed to function outside its operating conditions.



Typical Operating Characteristics





Operation

The SY6288C7/D7 is a current limited P-channel MOSFET power switch designed for high-side loadswitching applications. There is no parasitic body diode between drain and source of the MOSFET, so the SY6288C7/D7 prevents current flow from out to input when out being externally forced to a higher voltage than input when chip is disabled.

Over Current Protection

When the over current condition is sensed, the gate of the pass switch is modulated to achieve constant output current. Under output short circuit conditions, the normal current limit is folded back to 50%. If the over current condition persists for a long enough time, the junction temperature may exceed 150°C, and over temperature protection will shut down the IC. Once the chip temperature drops below 130°C, the IC will restart.

Short Circuit

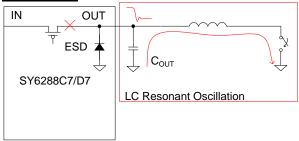


Figure 1. Equivalent Circuit

During short circuit, the parasitic inductor of the short wire and output capacitor will cause LC resonant oscillation thus cause negative voltage on OUT pin and current will flow through the ESD diode on OUT pin. This ESD diode can handle 600mA maximum forward current.

Fault Flag (OCB)

The OCB output is asserted (active low) when an over temperature shutdown condition or over current condition persists for 15ms. The output remains asserted until the over current or over temperature condition is removed. Connecting a heavy capacitive load to an enabled device can cause a momentary over

current condition; however, no false reporting on OCB occurs due to the 15ms deglitch circuit.

Supply Filter Capacitor

In order to prevent the input voltage drooping during hot plug events, a 1µF ceramic capacitor form IN to GND is strongly recommended. However, higher capacitor values could reduce the voltage droop on the input further. Furthermore, an output short will cause ringing on the input without the input capacitor. It could destroy the internal circuitry when the input transient exceeds 6V which is the absolute maximum supply voltage even for a short duration.

Output Filter Capacitor

Between OUT and GND, a low-ESR 150µF aluminum electrolytic or tantalum capacitor is strongly recommended to meet the 330mV maximum droop requirement. Standard bypass methods should be used to minimize inductance and resistance between the bypass capacitor and the downstream connector. This will reduce EMI and improve the transient performance.

PCB Lavout Guide

For best performance of the SY6288C7/D7, the following guidelines must be strictly followed:

- Keep all V_{BUS} traces as short and wide as possible and use at least 2 ounce copper for all V_{BUS} traces.
- Place a ground plane under all circuitry to lower both resistance and inductance and improve DC and transient performance.
- Locate the output capacitor as close to the connectors as possible to lower impedance (mainly inductance) between the port and the capacitor and improve transient performance.
- Input and output capacitors should be placed closed to the IC and connected to ground plane to reduce noise coupling.
- Locate the ceramic bypass capacitors as close as possible to the IN pins and OUT pins of SY6288C7/D7.



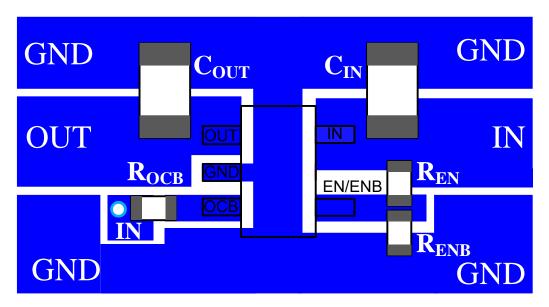
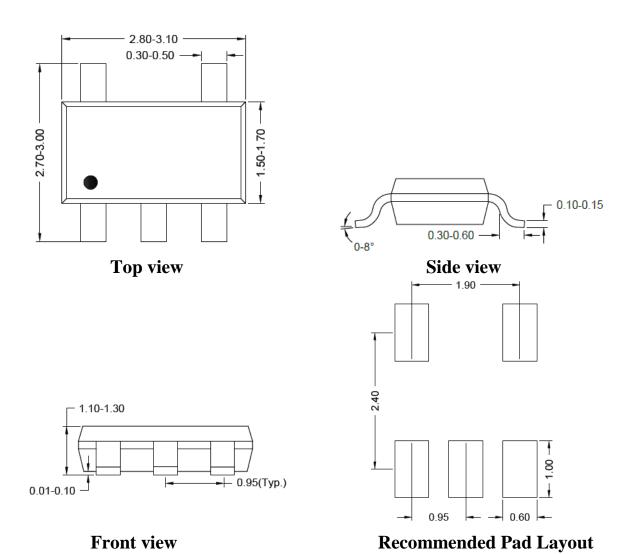


Figure 2. PCB Layout Suggestion



SOT23-5 Package Outline & PCB Layout

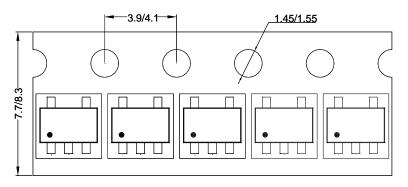


Notes: All dimension in millimeter and exclude mold flash & metal burr.



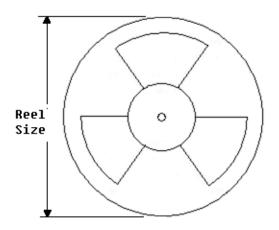
Taping & Reel Specification

1. Taping Orientation SOT23-5



Feeding direction →

2. Carrier Tape & Reel Specification for Packages



Package	Tape width (mm)	Pocket	Reel size	Trailer	Leader length	Qty per
types		pitch(mm)	(Inch)	length(mm)	(mm)	reel
SOT23-5	8	4	7''	280	160	3000

3. Others: NA



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